

Product Summary

Part #	V_{DS}	$R_{DS(on).typ}$ (@ $V_{GS}=10V$)	$R_{DS(on).typ}$ (@ $V_{GS}=4.5V$)	I_D
EFM3404A	30V	19m Ω	25m Ω	5A

Features

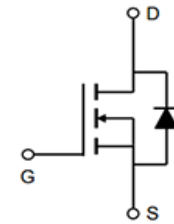
- Low $R_{DS(on)}$ @ $V_{GS}=10V$
- 4.5V Logic Level Control
- N Channel SOT23-3L Package
- Pb-Free, RoHS Compliant

Application

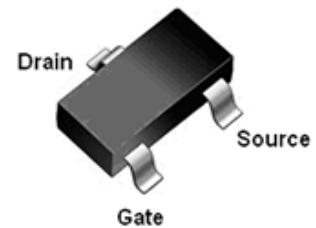
- DC-to-DC converters
- Power management
- Low-side load switch
- Switching circuits
- High-speed line driver

Ordering Information:

Part NO.	EFM3404A
Marking	A48K
Packing Information	REEL TAPE
Basic ordering unit (pcs)	3000



N-Channel MOSFET



SOT23-3L



Absolute Maximum Ratings ($T_C=25^\circ C$)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V_{DS}	30	V
Gate-Source Voltage	V_{GS}	± 20	V
Drain Current-Continuous	I_D	5	A
Drain Current-Pulsed ^(Note 1)	I_{DM}	20	A
Maximum Power Dissipation	P_D	1.5	W
Operating Junction and Storage Temperature Range	T_J, T_{STG}	-55 To 150	$^\circ C$

Thermal Characteristic

Thermal Resistance, Junction-to-Ambient ^(Note 2)	$R_{\theta JA}$	100	$^\circ C/W$
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• Static Electrical Characteristics @ T_J = 25°C (unless otherwise stated)

Parameter	Symbol	Condition	Min	Typ	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V I _D =250uA	30	--	--	V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =30V V _{GS} =0V	--	--	1	μA
Gate-Body Leakage Current	I _{GSS}	V _{GS} =±20V V _{DS} =0V	--	--	±100	nA
On Characteristics ^(Note 3)						
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} I _D =250uA	1.0	1.5	2.5	V
Drain-Source On-State Resistance	R _{DS(ON)}	V _{GS} =10V I _D =5A	--	19	24	mΩ
		V _{GS} =4.5V I _D =4A	--	25	40	mΩ
Dynamic Characteristics ^(Note4)						
Input Capacitance	C _{ISS}	V _{DS} =15V V _{GS} =0V F=1.0MHz	--	357	--	PF
Output Capacitance	C _{OSS}		--	52	--	PF
Reverse Transfer Capacitance	C _{RSS}		--	45	--	PF
Gate Resistance	R _g	F=1.0MHz	--	9	--	Ω
Switching Characteristics ^(Note 4)						
Turn-on Delay Time	t _{d(on)}	V _{DD} =15V I _D =5A V _{GS} =10V R _G =3.3Ω,	--	7	--	nS
Turn-on Rise Time	t _r		--	18	--	nS
Turn-Off Delay Time	t _{d(off)}		--	25	--	nS
Turn-Off Fall Time	t _f		--	11	--	nS
Total Gate Charge	Q _g	V _{DS} =15V I _D =5A V _{GS} =10V	--	6.7	--	nC
Gate-Source Charge	Q _{gs}		--	3.4	--	nC
Gate-Drain Charge	Q _{gd}		--	1.5	--	nC
Drain-Source Diode Characteristics						
Diode Forward Voltage ^(Note 3)	V _{SD}	V _{GS} =0V I _S =4A	--	0.79	1.2	V
Diode Forward Current ^(Note 2)	I _S		--	--	1.8	A

Notes:

- ① Pulse width limited by maximum allowable junction temperature
 ② Pulse test ; Pulse width≤300μs, duty cycle≤2%.

• Typical Characteristics

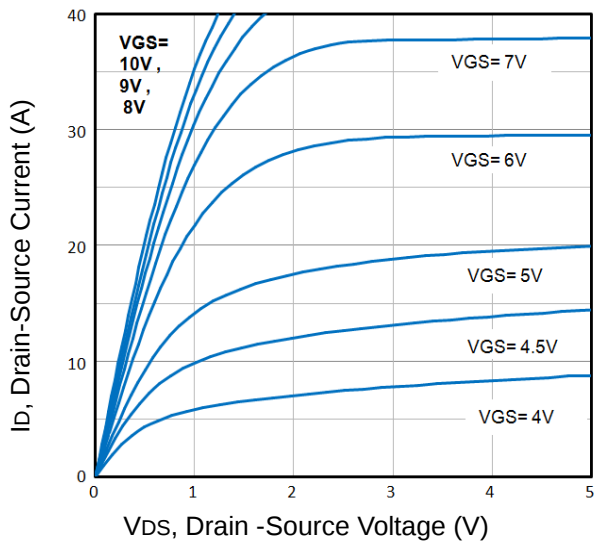


Fig1. Typical Output Characteristics

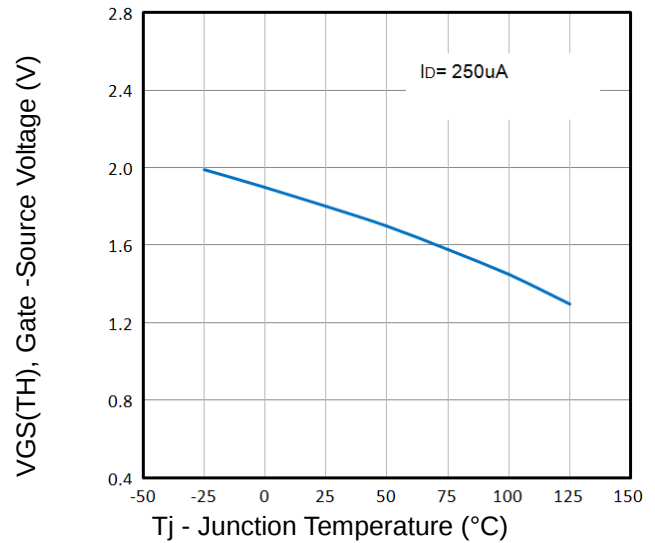


Fig2. Normalized Threshold Voltage Vs. Temperature

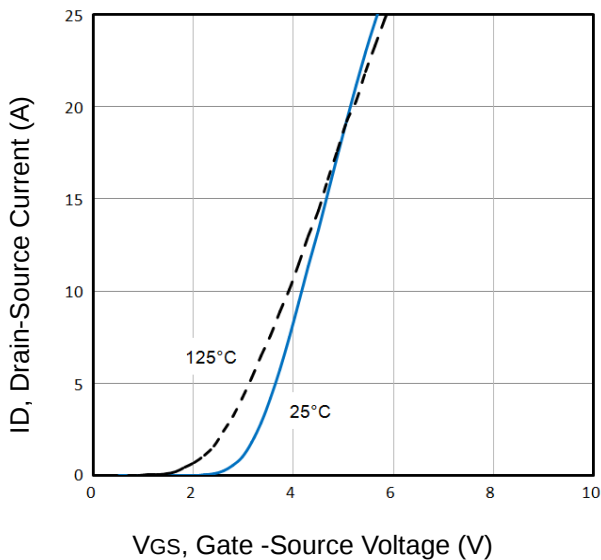


Fig3. Typical Transfer Characteristics

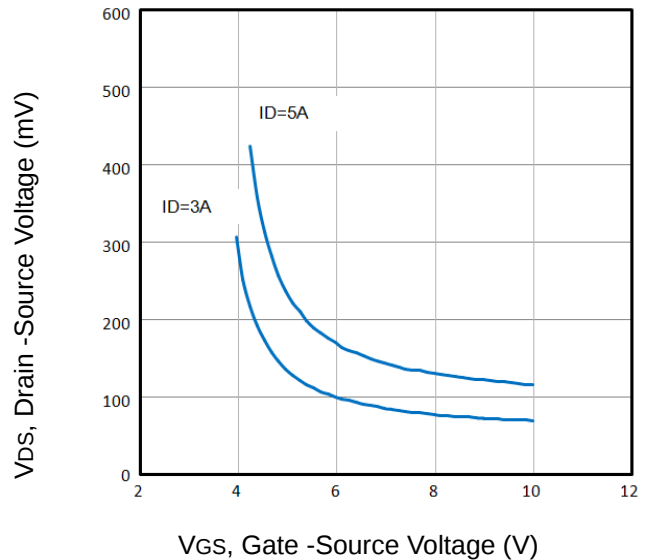


Fig4. Drain-Source Voltage vs Gate-Source Voltage

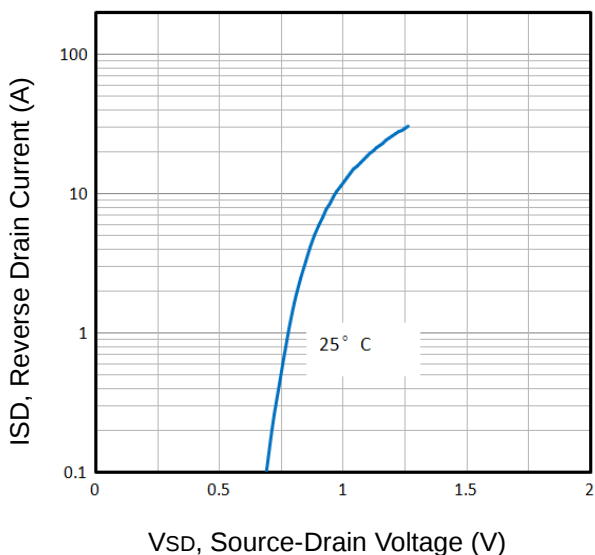


Fig5. Typical Source-Drain Diode Forward Voltage

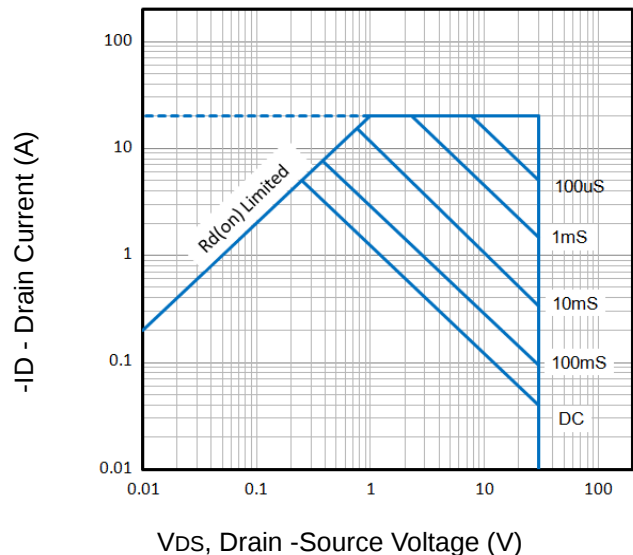


Fig6. Maximum Safe Operating Area

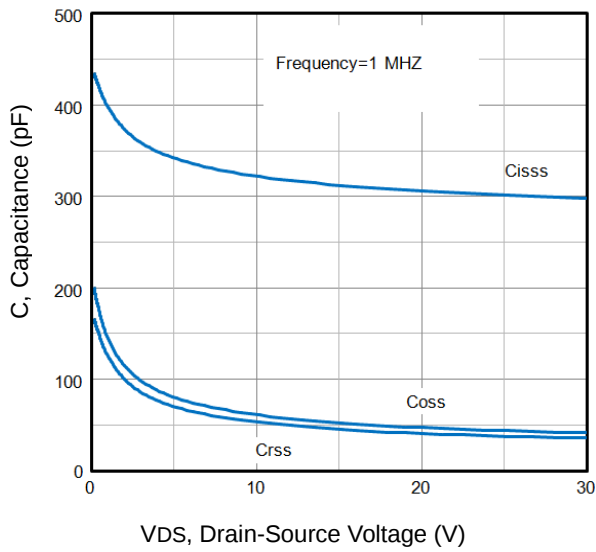


Fig7. Typical Capacitance Vs. Drain-Source Voltage

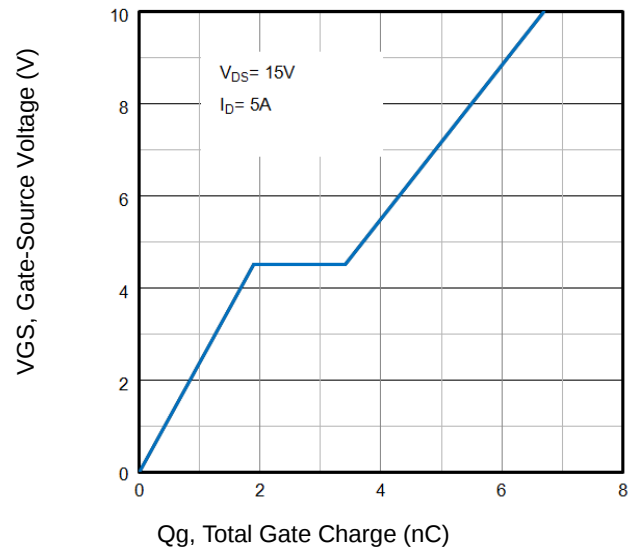


Fig8. Typical Gate Charge Vs. Gate-Source Voltage

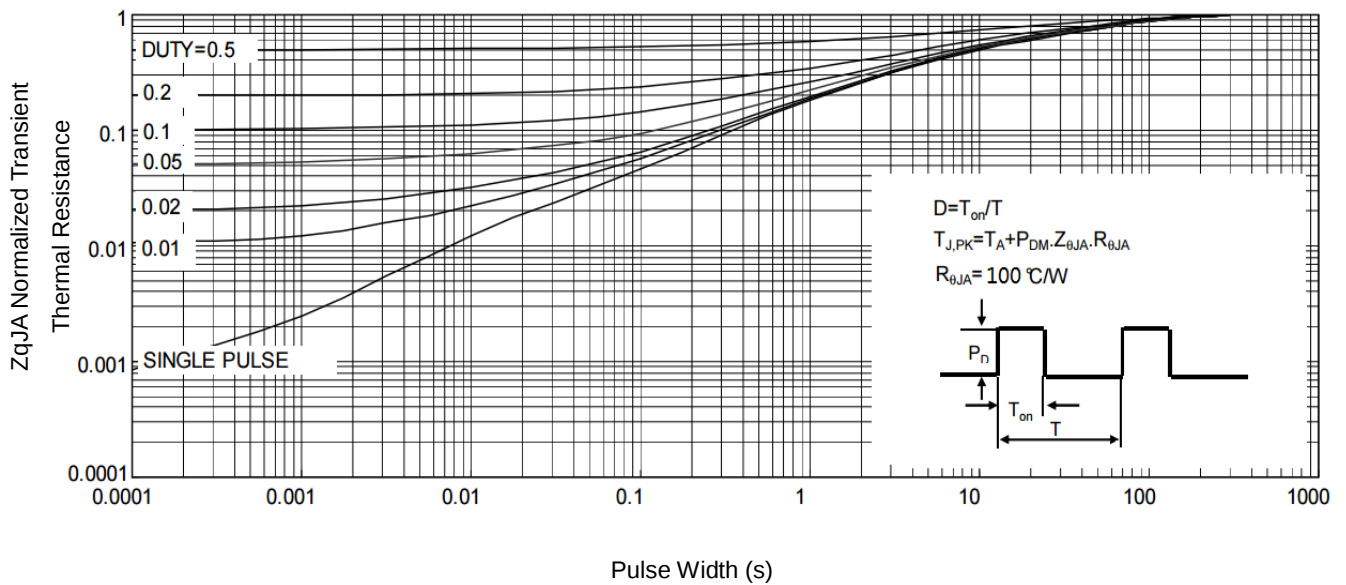


Fig9. Normalized Maximum Transient Thermal Impedance

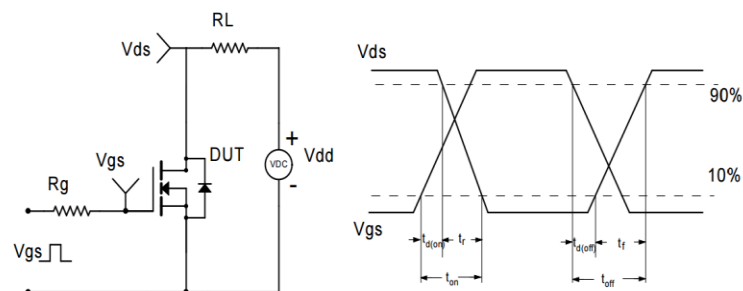
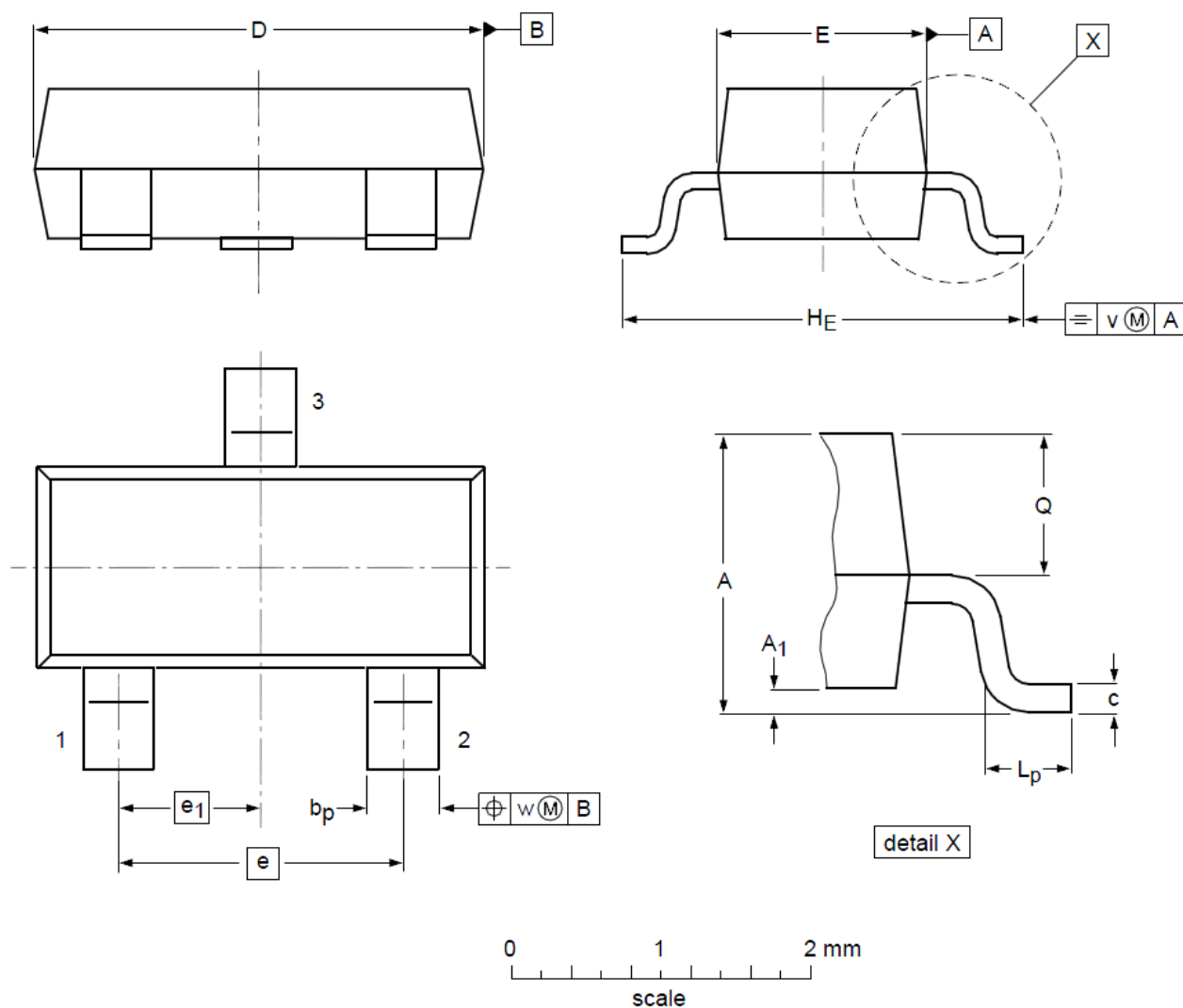


Fig10. Switching Time Test Circuit and waveforms

SOT23-3L Package Outline Dimensions



DIMENSIONS (unit : mm)

Symbol	Min	Typ	Max	Symbol	Min	Typ	Max
A	1.00	1.17	1.30	A ₁	0.01	0.05	0.10
b _p	0.35	0.39	0.50	c	0.10	0.20	0.26
D	2.70	2.90	3.10	E	1.30	1.58	1.70
e	--	1.90	--	e ₁	--	0.95	--
H _E	2.50	2.78	3.00	L _p	0.20	0.32	0.60
Q	0.23	0.27	0.33	v	--	0.20	--
w	--	0.20	--				