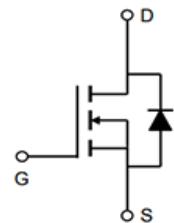


**• Product Summary**

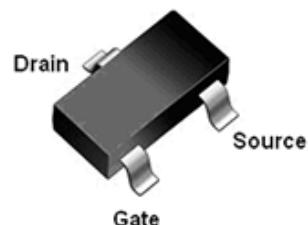
Part #	V <sub>DS</sub>	R <sub>DS(on).typ</sub> (@V <sub>GS</sub> =10V)	R <sub>DS(on).typ</sub> (@V <sub>GS</sub> =4.5V)	I <sub>D</sub>
EFM3406	30V	50mΩ	60mΩ	3.6A


**• Features**

- Low R<sub>DS(on)</sub> @V<sub>GS</sub>=10V
- 4.5V Logic Level Control
- N Channel SOT23 Package
- Pb-Free, RoHS Compliant

**N-Channel MOSFET**
**• Application**

- Load Switch
- Battery switch
- DC/DC Converter


**• Ordering Information:**

Part NO.	EFM3406
Marking	3406
Packing Information	REEL TAPE
Basic ordering unit (pcs)	3000

**HF**
**• Absolute Maximum Ratings (T<sub>C</sub>=25°C)**

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V <sub>DS</sub>	30	V
Gate-Source Voltage	V <sub>GS</sub>	±20	V
Drain Current-Continuous	I <sub>D</sub>	3.6	A
Drain Current-Pulsed (Note 1)	I <sub>DM</sub>	15	A
Maximum Power Dissipation	P <sub>D</sub>	1.4	W
Operating Junction and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-55 To 150	°C

**• Thermal Characteristic**

Thermal Resistance, Junction-to-Ambient (Note 2)	R <sub>θJA</sub>	125	°C/W
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**• Static Electrical Characteristics @  $T_J = 25^\circ\text{C}$  (unless otherwise stated)**

Parameter	Symbol	Condition	Min	Typ	Max	Unit
<b>Off Characteristics</b>						
Drain-Source Breakdown Voltage	$\text{BV}_{\text{DSS}}$	$V_{\text{GS}}=0\text{V} I_{\text{D}}=250\mu\text{A}$	30	--	--	V
Zero Gate Voltage Drain Current	$I_{\text{DSS}}$	$V_{\text{DS}}=30\text{V} V_{\text{GS}}=0\text{V}$	--	--	1	$\mu\text{A}$
Gate-Body Leakage Current	$I_{\text{GSS}}$	$V_{\text{GS}}=\pm 20\text{V} V_{\text{DS}}=0\text{V}$	--	--	$\pm 100$	nA
<b>On Characteristics</b> <small>(Note 3)</small>						
Gate Threshold Voltage	$V_{\text{GS(th)}}$	$V_{\text{DS}}=V_{\text{GS}} I_{\text{D}}=250\mu\text{A}$	1.0	1.6	2.5	V
Drain-Source On-State Resistance	$R_{\text{DS(ON)}}$	$V_{\text{GS}}=10\text{V} I_{\text{D}}=3.6\text{A}$	--	50	65	$\text{m}\Omega$
		$V_{\text{GS}}=4.5\text{V} I_{\text{D}}=3\text{A}$	--	60	75	$\text{m}\Omega$
<b>Dynamic Characteristics</b> <small>(Note 4)</small>						
Input Capacitance	$C_{\text{iss}}$	$V_{\text{DS}}=15\text{V} V_{\text{GS}}=0\text{V}$ $F=1.0\text{MHz}$	--	288	--	PF
Output Capacitance	$C_{\text{oss}}$		--	57	--	PF
Reverse Transfer Capacitance	$C_{\text{rss}}$		--	39	--	PF
Gate Resistance	$R_g$	$F=1.0\text{MHz}$	--	3	--	$\Omega$
<b>Switching Characteristics</b> <small>(Note 4)</small>						
Turn-on Delay Time	$t_{\text{d(on)}}$	$V_{\text{DD}}=15\text{V} I_{\text{D}}=3\text{A}$ $V_{\text{GS}}=10\text{V} R_G=3.3\Omega$	--	4.6	--	nS
Turn-on Rise Time	$t_r$		--	1.9	--	nS
Turn-Off Delay Time	$t_{\text{d(off)}}$		--	21	--	nS
Turn-Off Fall Time	$t_f$		--	2.6	--	nS
Total Gate Charge	$Q_g$	$V_{\text{DS}}=15\text{V} I_{\text{D}}=3.6\text{A}$ $V_{\text{GS}}=10\text{V}$	--	6.5	--	nC
Gate-Source Charge	$Q_{\text{gs}}$		--	1.2	--	nC
Gate-Drain Charge	$Q_{\text{gd}}$		--	1.6	--	nC
<b>Drain-Source Diode Characteristics</b>						
Diode Forward Voltage <small>(Note 3)</small>	$V_{\text{SD}}$	$V_{\text{GS}}=0\text{V} I_{\text{S}}=1\text{A}$	--	0.79	1	V
Diode Forward Current <small>(Note 2)</small>	$I_{\text{S}}$		--	--	2.5	A

**Notes:**

A: The value of  $R_{\text{0JA}}$  is measured with the device mounted on 1in FR-4 board with 2oz. Copper, in a still air environment with  $T_A=25^\circ\text{C}$ .

The value in any given application depends on the user's specific board design. The current rating is based on the  $t \leq 10\text{s}$  thermal resistance rating.

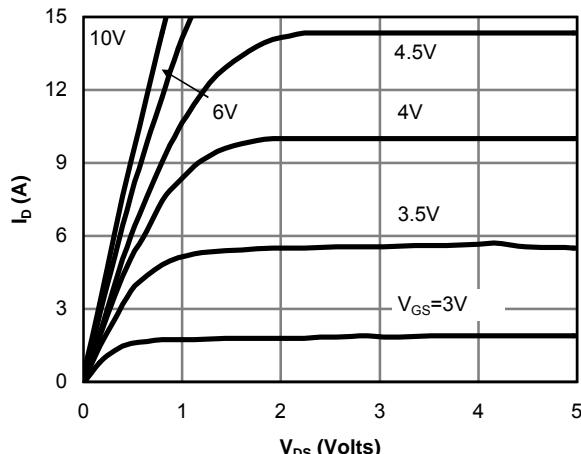
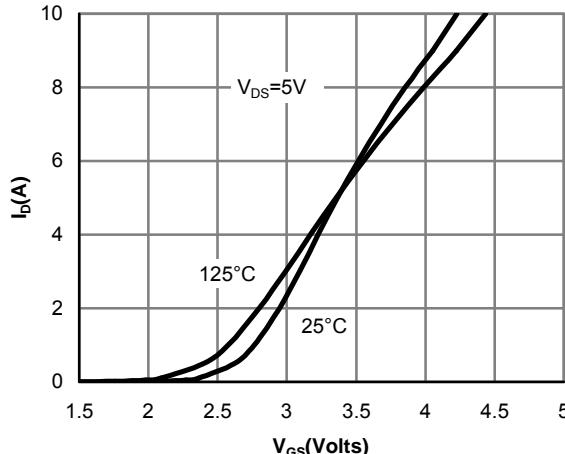
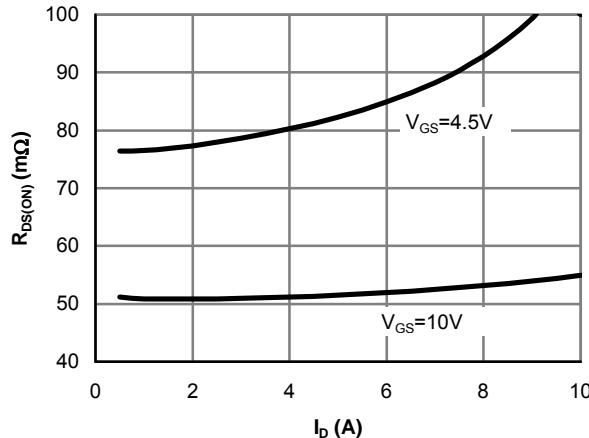
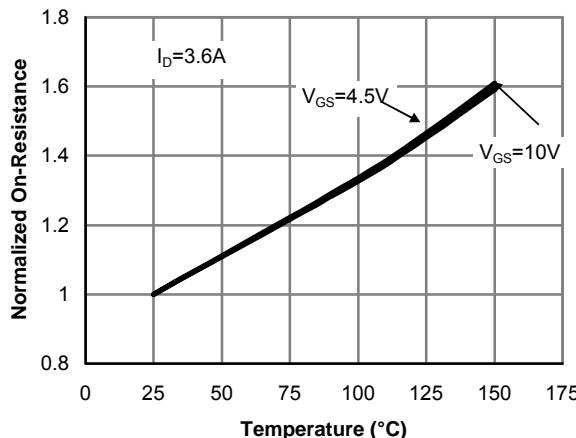
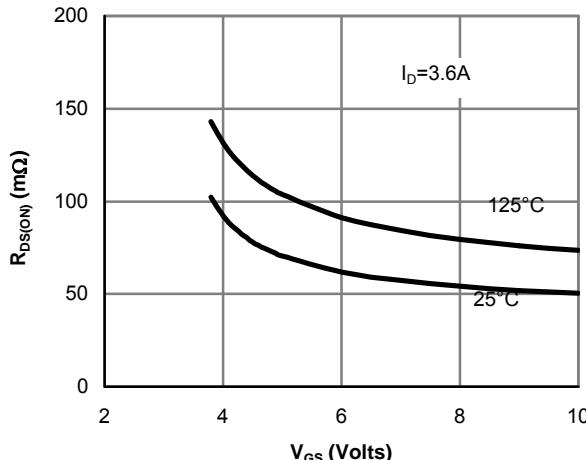
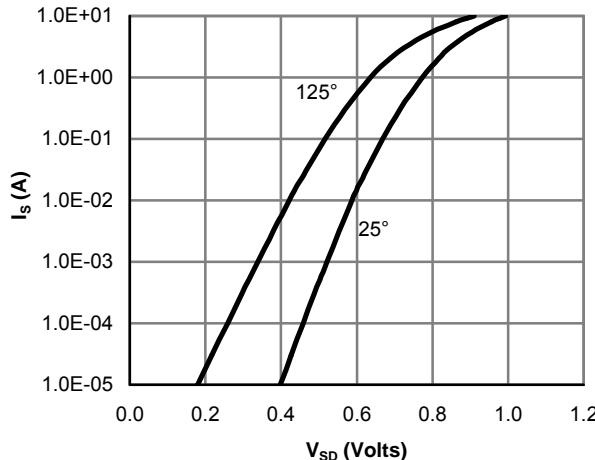
B: Repetitive rating, pulse width limited by junction temperature.

C: The  $R_{\text{0JA}}$  is the sum of the thermal impedance from junction to lead  $R_{\text{0JL}}$  and lead to ambient.

D: The static characteristics in Figures 1 to 6,12,14 are obtained using  $80\mu\text{s}$  pulses, duty cycle 0.5% max.

E: These tests are performed with the device mounted on 1 in<sup>2</sup> FR-4 board with 2oz. Copper, in a still air environment with  $T_A=25^\circ\text{C}$ . The SOA curve provides a single pulse rating.

## • Typical Characteristics


**Fig 1: On-Region Characteristics**

**Figure 2: Transfer Characteristics**

**Figure 3: On-Resistance vs. Drain Current and Gate Voltage**

**Figure 4: On-Resistance vs. Junction Temperature**

**Figure 5: On-Resistance vs. Gate-Source Voltage**

**Figure 6: Body-Diode Characteristics**

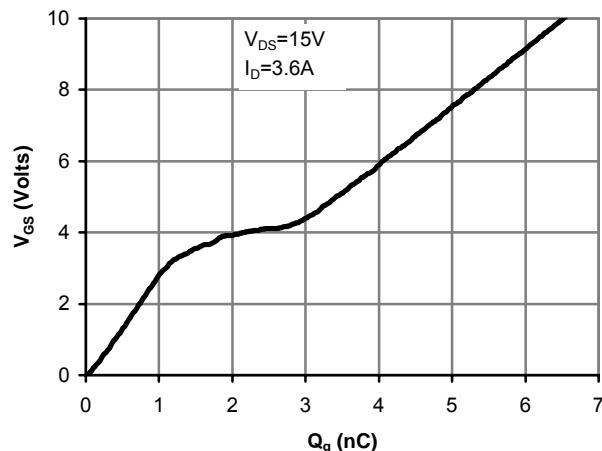


Figure 7: Gate-Charge Characteristics

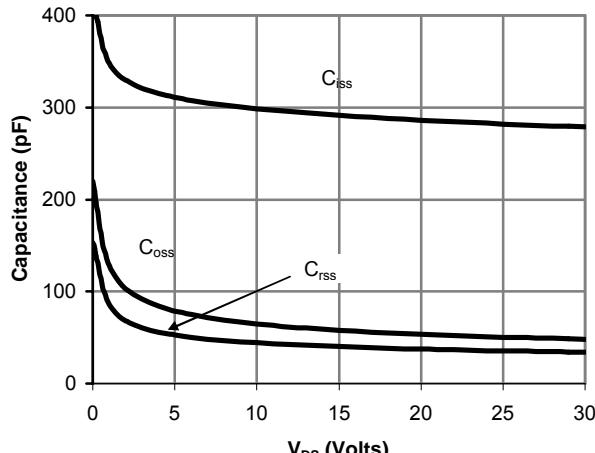


Figure 8: Capacitance Characteristics

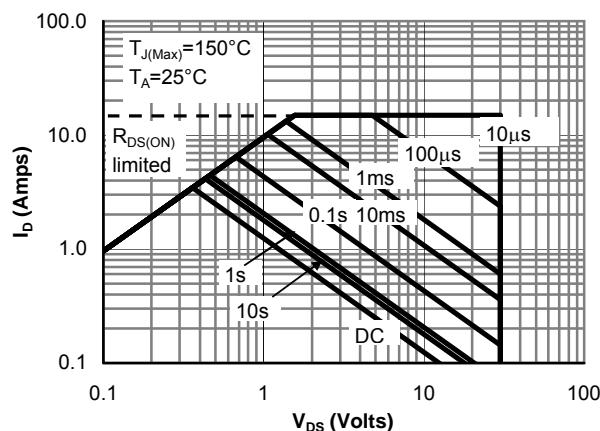


Figure 9: Maximum Forward Biased Safe Operating Area (Note E)

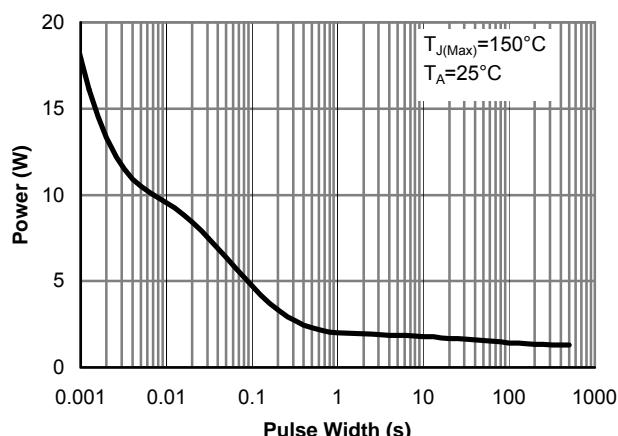


Figure 10: Single Pulse Power Rating Junction-to-Ambient (Note E)

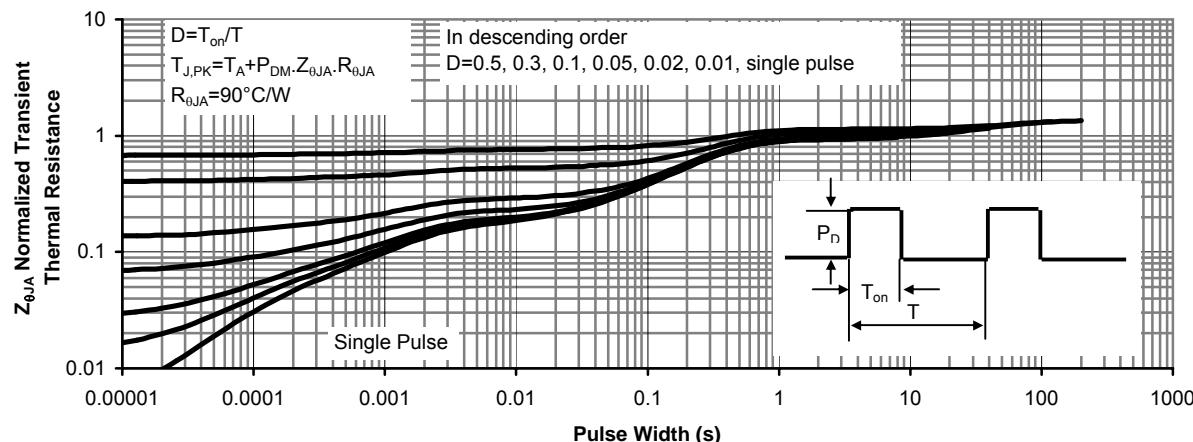
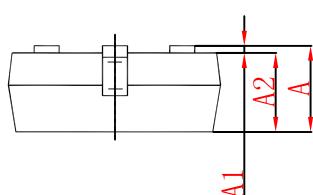
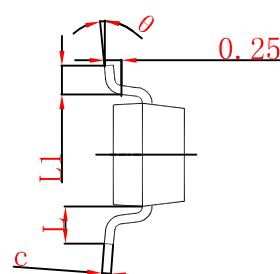
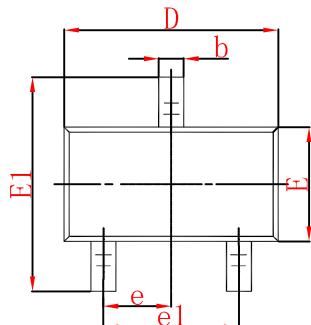
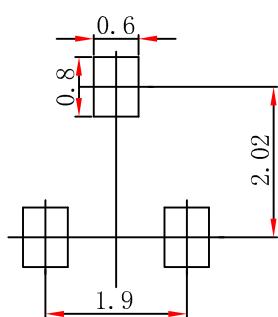


Figure 11: Normalized Maximum Transient Thermal Impedance

**SOT-23 Package Outline Dimensions**


Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	0.900	1.150	0.035	0.045
A1	0.000	0.100	0.000	0.004
A2	0.900	1.050	0.035	0.041
b	0.300	0.500	0.012	0.020
c	0.080	0.150	0.003	0.006
D	2.800	3.000	0.110	0.118
E	1.200	1.400	0.047	0.055
E1	2.250	2.550	0.089	0.100
e	0.950 TYP		0.037 TYP	
e1	1.800	2.000	0.071	0.079
L	0.550 REF		0.022 REF	
L1	0.300	0.500	0.012	0.020
θ	0°	8°	0°	8°


**Note:**

1. Controlling dimension: in millimeters.
2. General tolerance:  $\pm 0.05\text{mm}$ .
3. The pad layout is for reference purposes only.