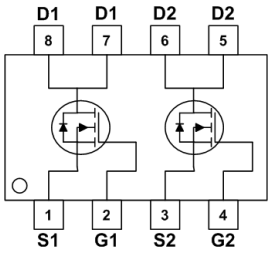
EFMICRO
Semiconductor Limited Company

EFM4812A

Dual N-Channel Advanced MOSFET

• Product Summary

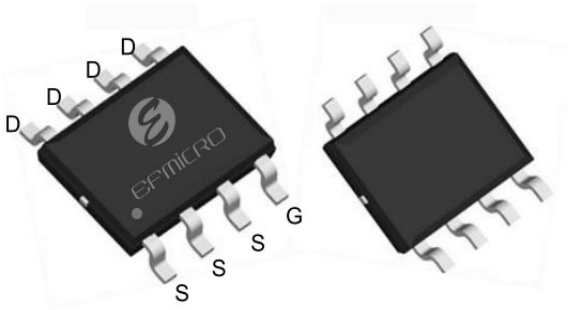
Part #	V_{DS}	$R_{DS(on).typ}$ ($@V_{GS}$=10V)	$R_{DS(on).typ}$ ($@V_{GS}$=4.5V)	I_D
EFM4812A	30V	15mΩ	21mΩ	8.5A



Dual N-Channel MOSFET

• Description

- The EFM4812A is the high cell density trenched
- N-ch MOSFETs which provide excellent
- RDSON and gate cherge for most of the
- synchronous buck converter applications.
- The EFM4812A meet the RoHS and Green
- Prod uct requirement, 100 % EAS guaranteed
- with full function reliability approved.



SOP- 8

•Application

- Super Low Gate Charge 100% EAS Guaranteed
- Green Device Available Excellent CdV/dt effect decline
- Advanced high cell density Trench technology

•Ordering Information:

Part NO.	EFM4812A
Marking	4812 ******
Packing Information	REEL TAPE
Basic ordering unit (pcs)	3000

•Absolute Maximum Ratings (T_C=25℃)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V_{DS}	30	V
Gate-Source Voltage	V_{GS}	±20	V
Drain Current-Continuous	I_D	8.5	A
Drain Current-Pulsed (Note 1)	I_{DM}	37	A
Maximum Power Dissipation	P_D	1.5	W
Operating Junction and Storage Temperature Range	T_J,T_{STG}	-55 To 150	℃

•Thermal Characteristic

Thermal Resistance,Junction-to-Ambient (Note 2)	$R_{\theta JA}$	85	℃/W

● Static Electrical Characteristics @ T_J = 25°C (unless otherwise stated)

Parameter	Symbol	Condition	Min	Typ	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV_{DSS}	V_{GS}=0V I_D=250uA	30	--	--	V
Zero Gate Voltage Drain Current	I_{DSS}	V_{DS}=30V V_{GS}=0V	--	--	1	μA
Gate-Body Leakage Current	I_{GSS}	V_{GS}=±20V V_{DS}=0V	--	--	±100	nA
On Characteristics (Note 3)						
Gate Threshold Voltage	$V_{GS(th)}$	V_{DS}=V_{GS} I_D=250uA	1.2	--	2.5	V
Drain-Source On-State Resistance	$R_{DS(ON)}$	V_{GS}=10V I_D=8A	--	15	18	mΩ
		V_{GS}=4.5V I_D=6A	--	21	26	mΩ
Forward Transconductance	g_{FS}	V_{DS}=5V I_D=8A	--	30	--	S
Gate Resistance	R_g	F=1.0MHz	--	2.5	--	Ω
Dynamic Characteristics (Note4)						
Input Capacitance	C_{lss}	V_{DS}=15V V_{GS}=0V F=1.0MHz	--	740	--	PF
Output Capacitance	C_{oss}		--	110	--	PF
Reverse Transfer Capacitance	C_{rss}		--	82	--	PF
Switching Characteristics (Note 4)						
Turn-on Delay Time	$t_{d(on)}$	V_{DD}=15V I_D=8A V_{GS}=10V R_G=3.3Ω,	--	2.4	--	nS
Turn-on Rise Time	t_r		--	7.8	--	nS
Turn-Off Delay Time	$t_{d(off)}$		--	22	--	nS
Turn-Off Fall Time	t_f		--	4	--	nS
Total Gate Charge	Q_g	V_{DS}=15V I_D=-8A V_{GS}=10V	--	6	--	nC
Gate-Source Charge	Q_{gs}		--	2.5	--	nC
Gate-Drain Charge	Q_{gd}		--	2.1	--	nC
Drain-Source Diode Characteristics						
Diode Forward Voltage (Note 3)	V_{SD}	V_{GS}=0V I_S=8A	--	0.85	1.2	V
Diode Forward Current (Note 2)	I_S		--	--	8	A

Note :
1.The data tested by surface mounted on a 1 inch^2 FR-4 board with 2OZ copper.
2.The data tested by pulsed , pulse width ≦ 300us , duty cycle ≦ 2%
3.The EAS data shows Max. rating . The test condition is V_{DD}=25V,V_{GS}=10V,L=0.1mH,I_{AS}=21A
4.The power dissipation is limited by 150℃ junction temperature
5.The data is theoretically the same as I_D and I_{DM} , in real applications , should be limited by total power dissipation.

• Typical Characteristics

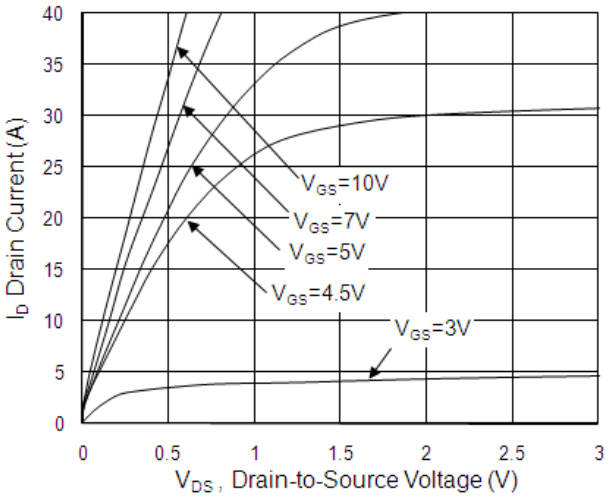


Fig.1 Typical Output Characteristics

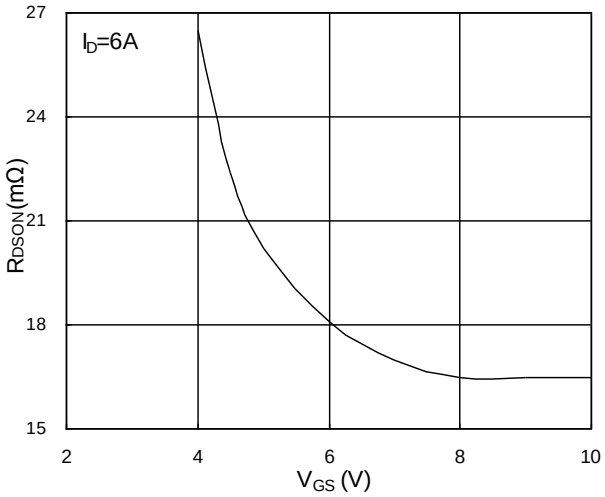


Fig.2 On-Resistance vs. G-S Voltage

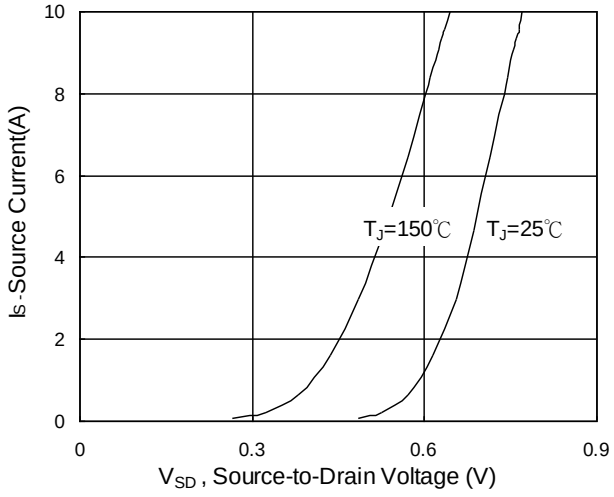


Fig.3 Forward Characteristics Of Reverse

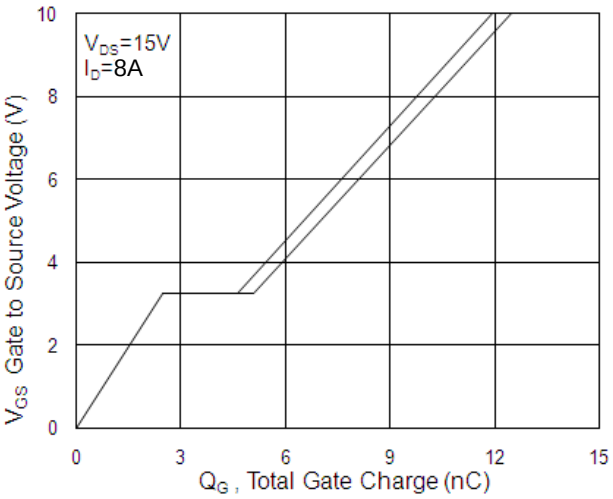


Fig.4 Gate-Charge Characteristics

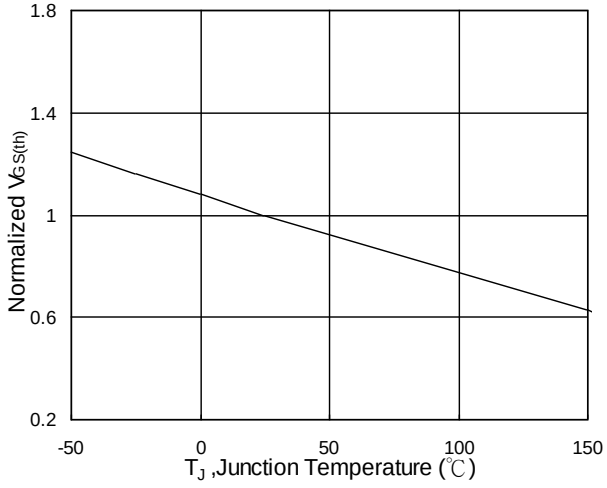


Fig.5 Normalized $V_{GS(th)}$ vs. T_J

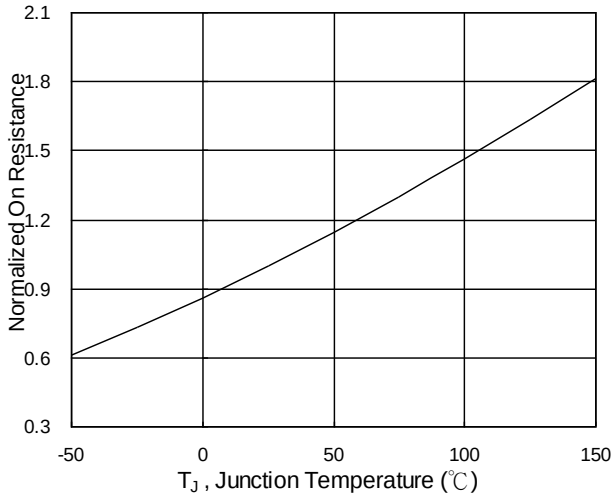


Fig.6 Normalized R_{DSON} vs. T_J

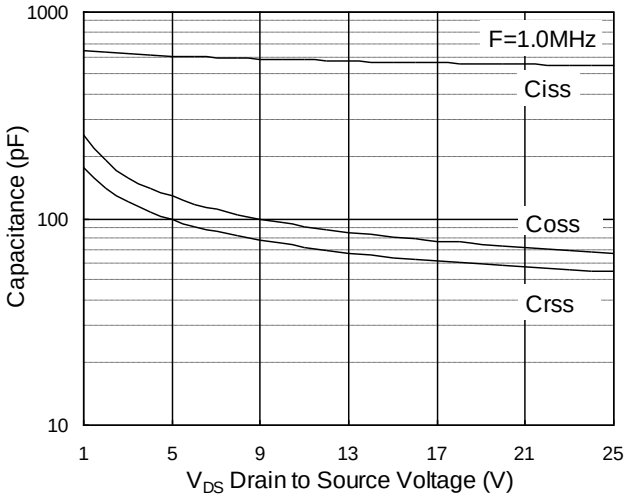


Fig.7 Capacitance

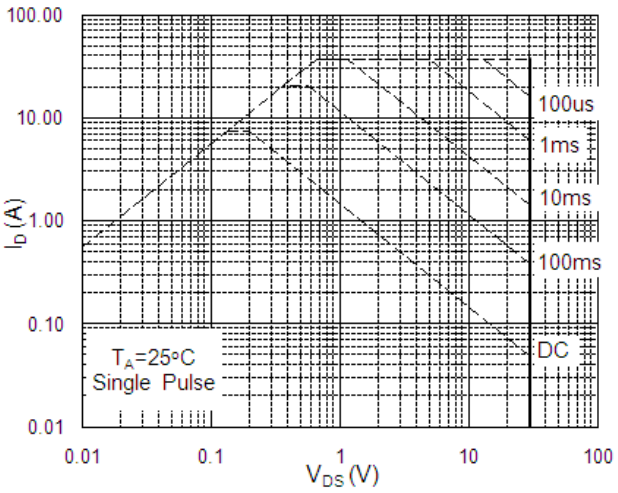


Fig.8 Safe Operating Area

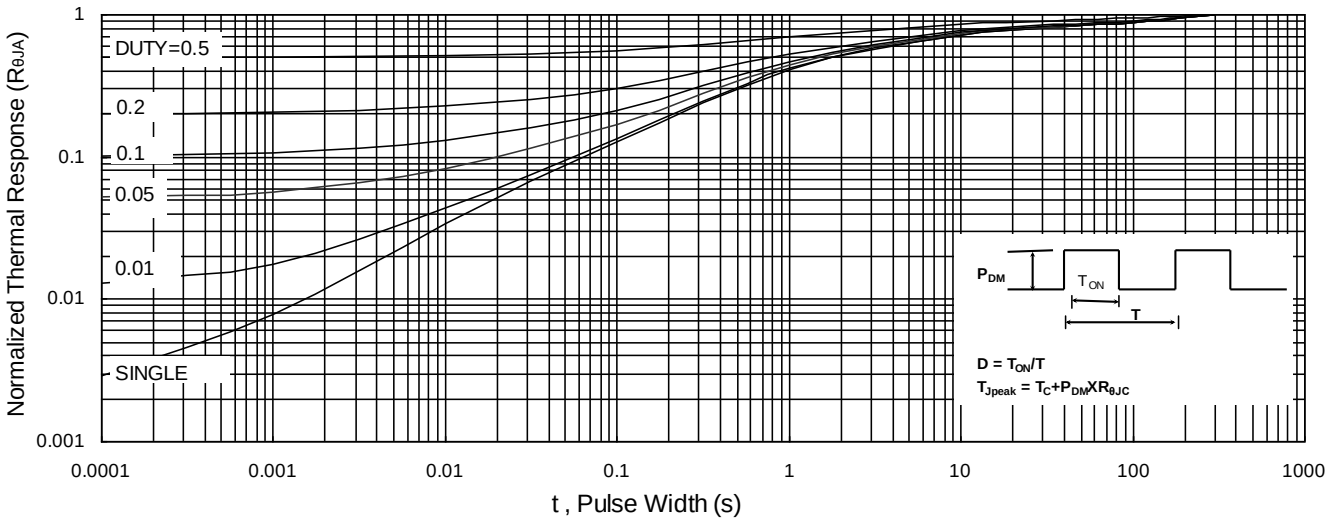


Fig.9 Normalized Maximum Transient Thermal Impedance

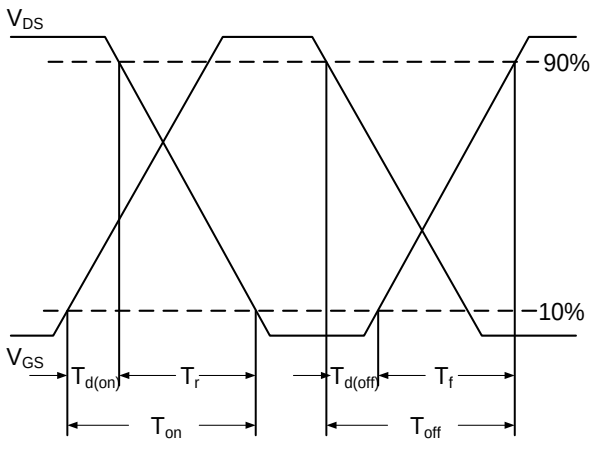


Fig.10 Switching Time Waveform

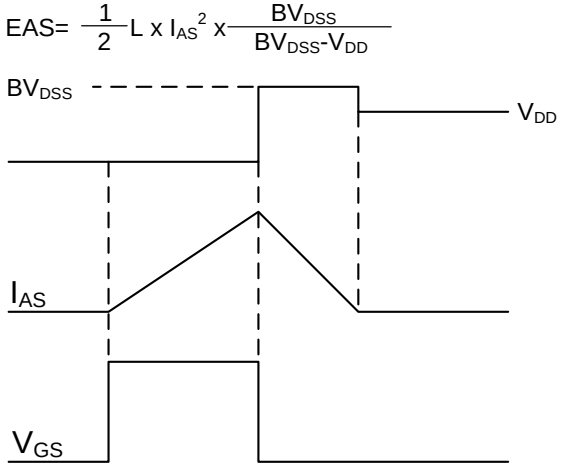
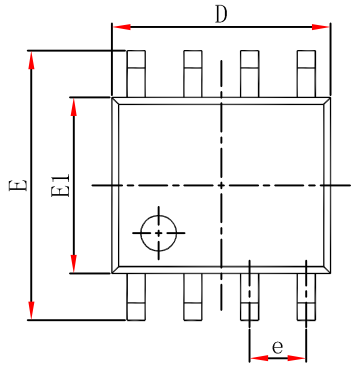
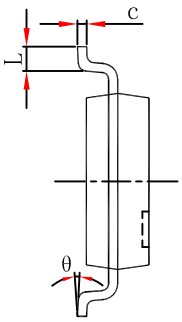
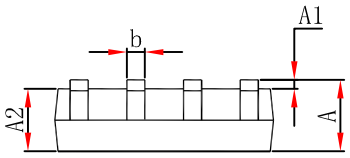


Fig.11 Unclamped Inductive Switching Waveform

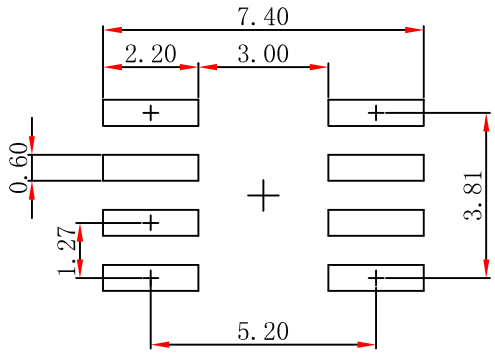
SOP8 Package Outline Dimensions







Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	**Max**	**Min**	**Max**
A	1.450	1.750	0.053	0.069
A1	0.100	0.250	0.004	0.010
A2	1.350	1.550	0.053	0.061
b	0.330	0.510	0.013	0.020
c	0.170	0.250	0.007	0.010
D	4.700	5.100	0.185	0.201
e	1.270（BSC）		0.050（BSC）	
E	5.800	6.200	0.228	0.244
E1	3.800	4.000	0.150	0.157
L	0.400	1.270	0.016	0.050
θ	0°	8°	0°	8°



Note:
1.Controlling dimension:in millimeters.
2.General tolerance:± 0.05mm.
3.The pad layout is for reference purposes only.