

### • Product Summary

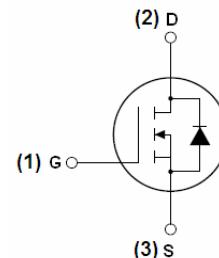
Part #	V <sub>DS</sub>	R <sub>DS(on).typ</sub> (@V <sub>GS</sub> =10V)	R <sub>DS(on).typ</sub> (@V <sub>GS</sub> =4.5V)	I <sub>D</sub>
EFM040N03F	30V	4mΩ	6.5mΩ	80A

### • Description

- The EFM040N03F is the high cell density trenched
- N-ch MOSFETs which provide excellent
- RDSON and gate charge for most of the
- synchronous buck converter applications.
- The EFM040N03F meet the RoHS and Green
- Product requirement, 100 % EAS guaranteed
- with full function reliability approved.

### • Application

- Super Low Gate Charge 100% EAS Guaranteed
- Green Device Available Excellent CdV/dt effect decline
- Advanced high cell density Trench technology


**N-Channel MOSFET**

**PDFN5\*6-8L**

### • Ordering Information:

Part NO.	EFM040N03F
Marking	040N03F *****
Packing Information	REEL TAPE
Basic ordering unit (pcs)	5000

### • Absolute Maximum Ratings (T<sub>C</sub>=25°C)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V <sub>DS</sub>	30	V
Gate-Source Voltage	V <sub>GS</sub>	±20	V
Drain Current-Continuous	I <sub>D</sub>	80	A
Drain Current-Pulsed <sup>(Note 1)</sup>	I <sub>DM</sub>	180	A
Maximum Power Dissipation	P <sub>D</sub>	48	W
Operating Junction and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-55 To 150	°C

### • Thermal Characteristic

Thermal Resistance, Junction-to-Ambient <sup>(Note 2)</sup>	R <sub>θJC</sub>	1.92	°C/W
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**• Static Electrical Characteristics @  $T_J = 25^\circ\text{C}$  (unless otherwise stated)**

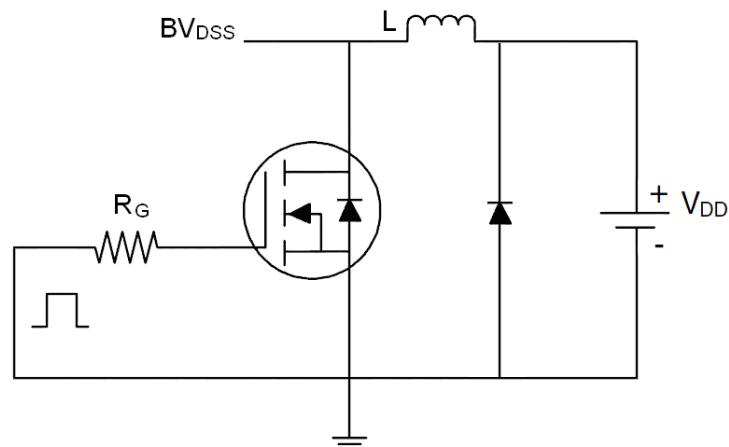
Parameter	Symbol	Condition	Min	Typ	Max	Unit
<b>Off Characteristics</b>						
Drain-Source Breakdown Voltage	$\text{BV}_{\text{DSS}}$	$V_{\text{GS}}=0\text{V} I_{\text{D}}=250\mu\text{A}$	30	--	--	V
Zero Gate Voltage Drain Current	$I_{\text{DSS}}$	$V_{\text{DS}}=30\text{V} V_{\text{GS}}=0\text{V}$	--	--	1	nA
Gate-Body Leakage Current	$I_{\text{GSS}}$	$V_{\text{GS}}=\pm 20\text{V} V_{\text{DS}}=0\text{V}$	--	--	$\pm 100$	nA
<b>On Characteristics</b> <small>(Note 3)</small>						
Gate Threshold Voltage	$V_{\text{GS(th)}}$	$V_{\text{DS}}=V_{\text{GS}} I_{\text{D}}=250\mu\text{A}$	1.0	1.6	2.5	V
Drain-Source On-State Resistance	$R_{\text{DS(ON)}}$	$V_{\text{GS}}=10\text{V} I_{\text{D}}=20\text{A}$	--	4	5	$\text{m}\Omega$
		$V_{\text{GS}}=4.5\text{V} I_{\text{D}}=20\text{A}$	--	6.5	8	$\text{m}\Omega$
Forward Transconductance	$g_{\text{FS}}$	$V_{\text{DS}}=5\text{V} I_{\text{D}}=20\text{A}$	20	--	--	S
<b>Dynamic Characteristics</b> <small>(Note 4)</small>						
Input Capacitance	$C_{\text{iss}}$	$V_{\text{DS}}=15\text{V} V_{\text{GS}}=0\text{V}$ $F=1.0\text{MHz}$	--	1400	--	PF
Output Capacitance	$C_{\text{oss}}$		--	205	--	PF
Reverse Transfer Capacitance	$C_{\text{rss}}$		--	177	--	PF
<b>Switching Characteristics</b> <small>(Note 4)</small>						
Turn-on Delay Time	$t_{\text{d(on)}}$	$V_{\text{DD}}=15\text{V} I_{\text{D}}=20\text{A}$ $V_{\text{GS}}=10\text{V} R_{\text{G}}=6\Omega$	--	9	--	nS
Turn-on Rise Time	$t_{\text{r}}$		--	8	--	nS
Turn-Off Delay Time	$t_{\text{d(off)}}$		--	28	--	nS
Turn-Off Fall Time	$t_{\text{f}}$		--	5	--	nS
Total Gate Charge	$Q_{\text{g}}$	$V_{\text{DS}}=15\text{V} I_{\text{D}}=20\text{A}$ $V_{\text{GS}}=10\text{V}$	--	32	--	nC
Gate-Source Charge	$Q_{\text{gs}}$		--	4.9	--	nC
Gate-Drain Charge	$Q_{\text{gd}}$		--	6.9	--	nC
<b>Drain-Source Diode Characteristics</b>						
Diode Forward Voltage <small>(Note 3)</small>	$V_{\text{SD}}$	$V_{\text{GS}}=0\text{V} I_{\text{S}}=20\text{A}$	--	0.85	1.2	V
Diode Forward Current <small>(Note 2)</small>	$I_{\text{S}}$		--	--	65	A

**Notes:**

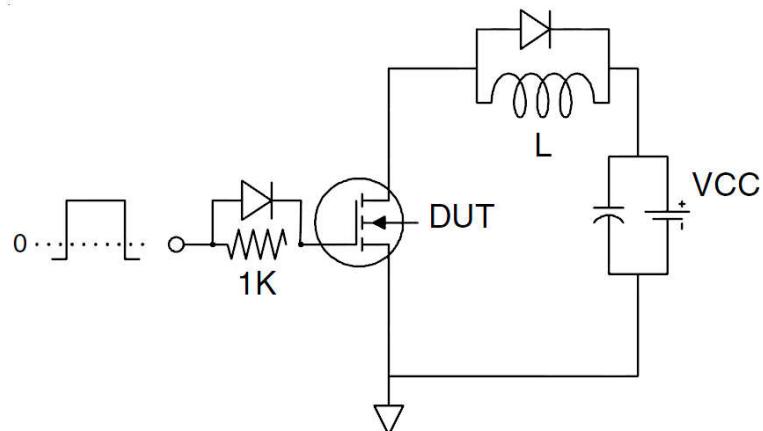
1. Repetitive Rating: Pulse width limited by maximum junction temperature.
2. Surface Mounted on FR4 Board,  $t \leq 10$  sec.
3. Pulse Test: Pulse Width  $\leq 300\mu\text{s}$ , Duty Cycle  $\leq 2\%$ .
4. Guaranteed by design, not subject to production
5. EAS condition:  $T_J=25^\circ\text{C}, V_{\text{DD}}=15\text{V}, V_G=10\text{V}, L=0.5\text{mH}, R_g=25\Omega$

• Typical Characteristics

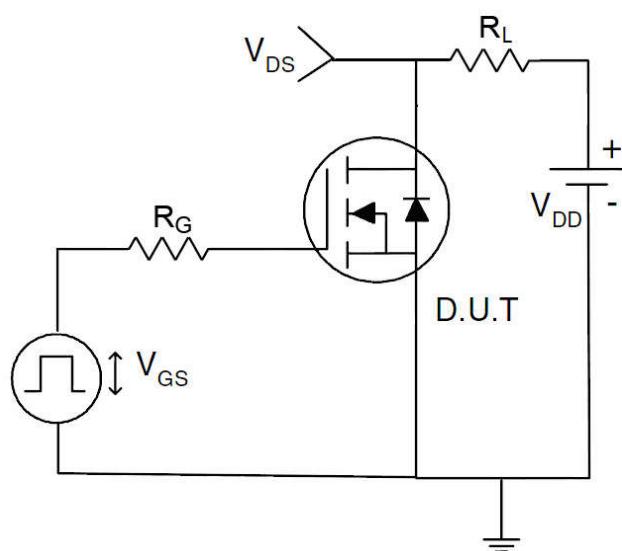
1)  $E_{AS}$  Test Circuits

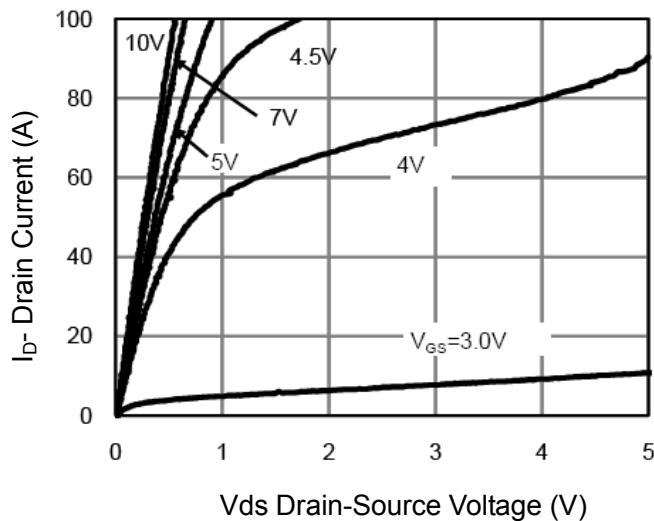
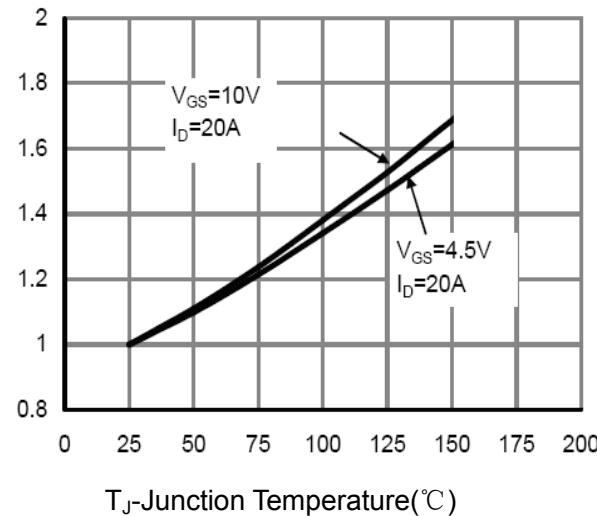
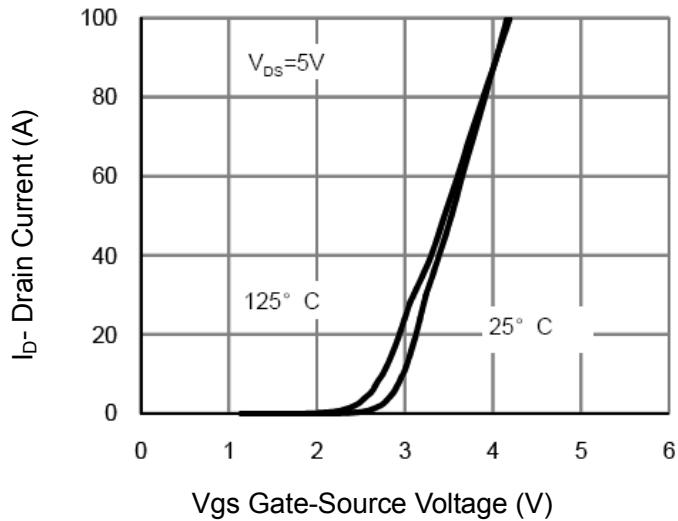
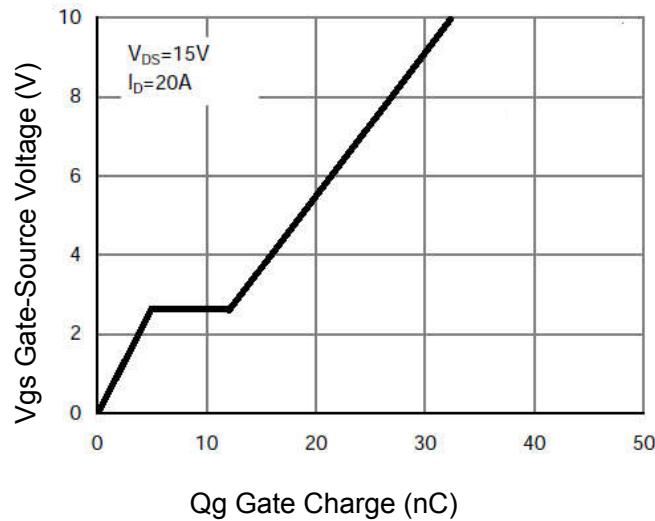
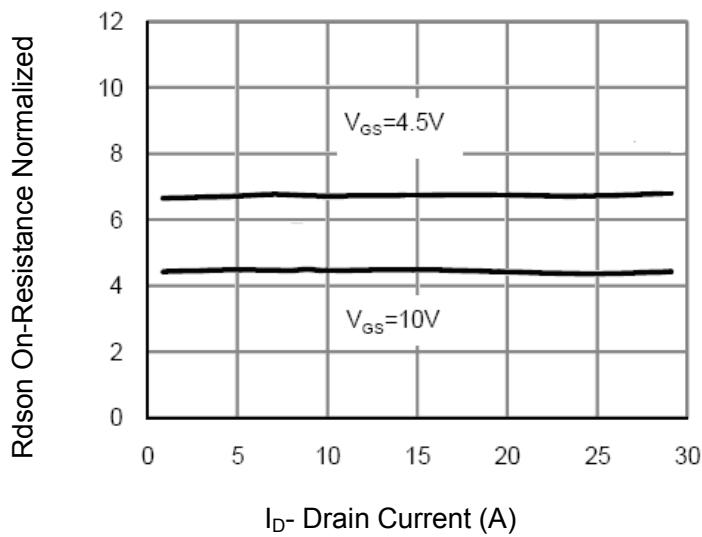
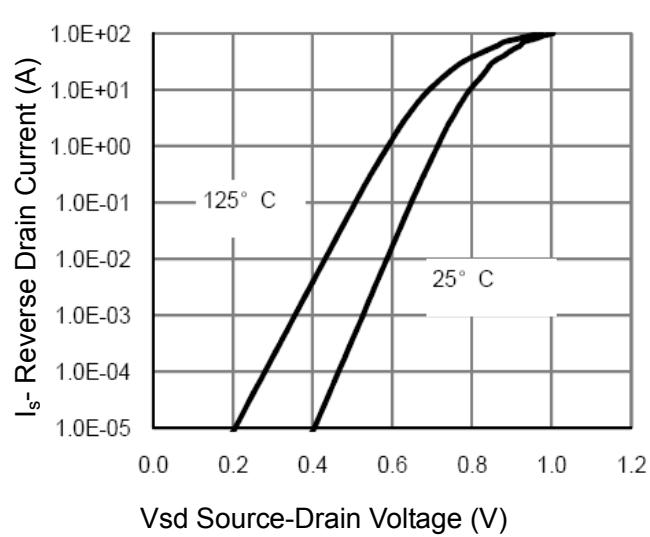


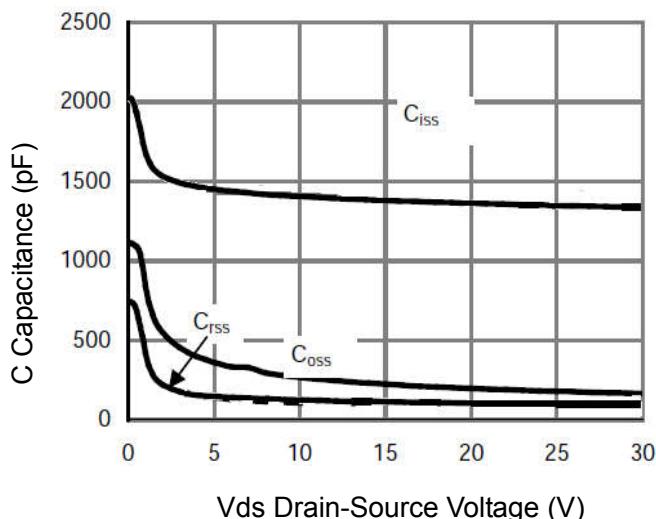
2) Gate Charge Test Circuit



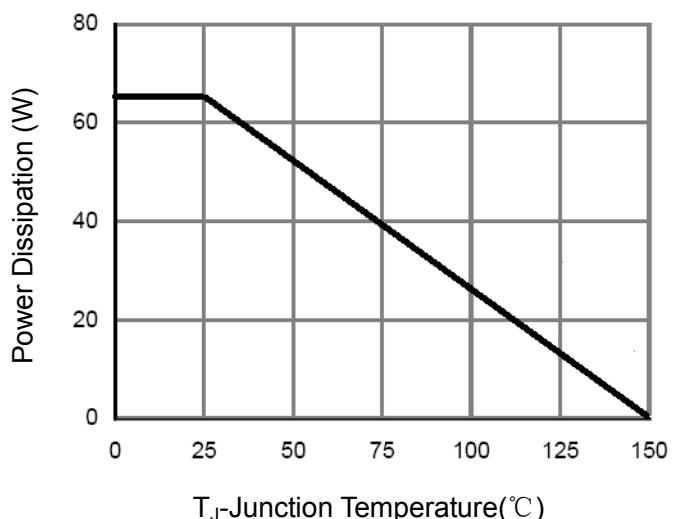
3) Switch Time Test Circuit



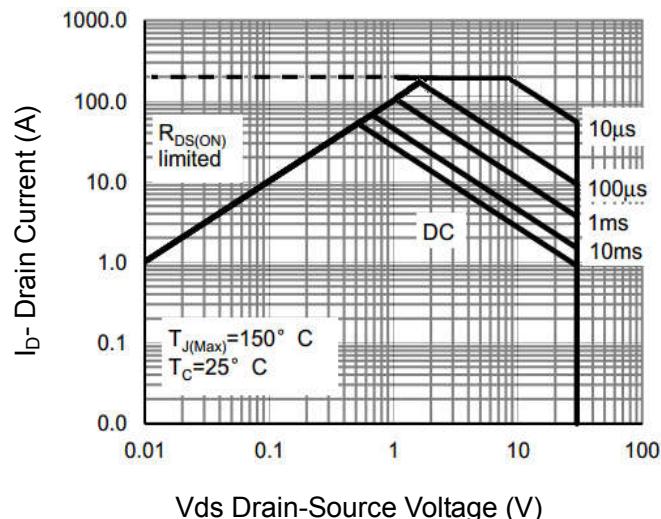

**Figure 1 Output Characteristics**

**Figure 4 Rdson-JunctionTemperature**

**Figure 2 Transfer Characteristics**

**Figure 5 Gate Charge**

**Figure 3 Rdson- Drain Current**

**Figure 6 Source- Drain Diode Forward**



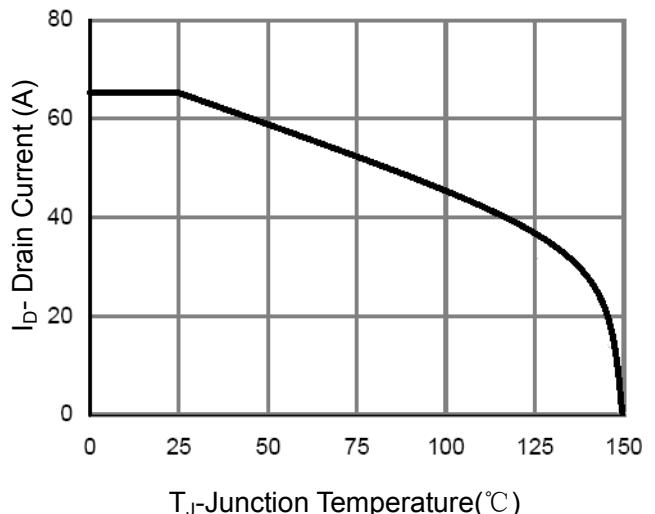
**Figure 7 Capacitance vs Vds**



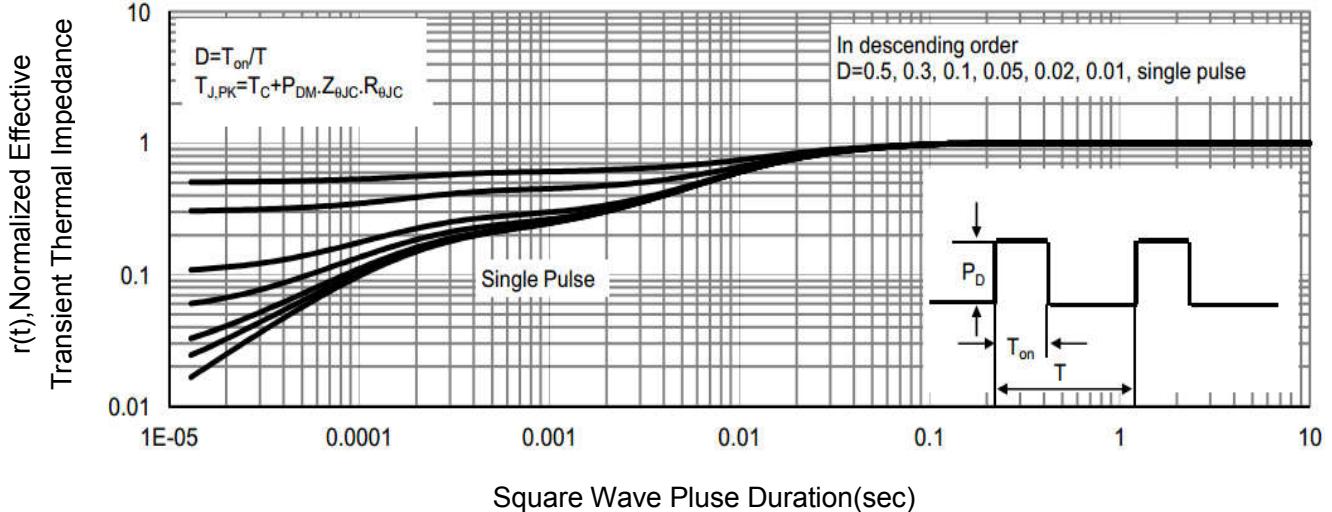
**Figure 9 Power De-rating**



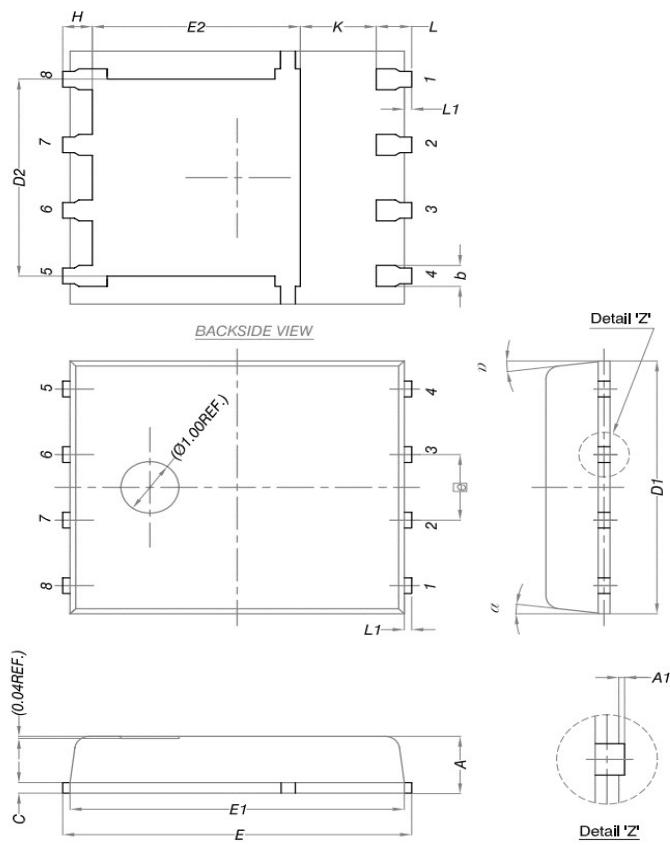
**Figure 8 Safe Operation Area**



**Figure 10 ID Current- Junction Temperature**



**Figure 11 Normalized Maximum Transient Thermal Impedance**

**•DFN5\*6 Package Outline**


DIM.	MILLIMETERS		
	MIN.	NOM.	MAX.
$A$	0.90	1.00	1.10
$A1$	0	-	0.05
$b$	0.33	0.41	0.51
$C$	0.20	0.25	0.30
$D1$	4.80	4.90	5.00
$D2$	3.61	3.81	3.96
$E$	5.90	6.00	6.10
$E1$	5.70	5.75	5.80
$E2$	3.38	3.58	3.78
$e$	1.27 BSC		
$H$	0.41	0.51	0.61
$K$	1.10	-	-
$L$	0.51	0.61	0.71
$L1$	0.06	0.13	0.20
$\alpha$	$0^\circ$	-	$12^\circ$