

### Product Summary

Part #	$V_{DS}$	$R_{DS(on).typ}$ (@ $V_{GS}=10V$ )	$R_{DS(on).typ}$ (@ $V_{GS}=4.5V$ )	$I_D$
EFM3459A	-30V	77m $\Omega$	85m $\Omega$	-2.6A

### Features

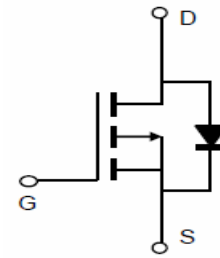
- Low  $R_{DS(on)}$  @ $V_{GS}=-10V$
- - 4.5V Logic Level Control
- P Channel SOT23-3L Package
- Pb-Free, RoHS Compliant

### Application

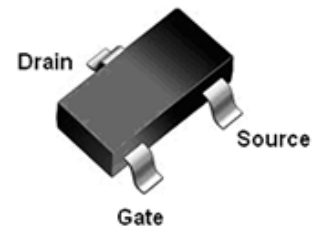
- Notebook
- Load Switch
- Battery Protection
- Hand-Held Instruments

### Ordering Information:

Part NO.	EFM3459A
Marking	BZ****
Packing Information	REEL TAPE
Basic ordering unit (pcs)	3000



P-Channel MOSFET



SOT23-3L



### Absolute Maximum Ratings ( $T_C=25^\circ C$ )

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	$V_{DS}$	-30	V
Gate-Source Voltage	$V_{GS}$	$\pm 20$	V
Drain Current-Continuous	$I_D$	-2.6	A
Drain Current-Pulsed <sup>(Note 1)</sup>	$I_{DM}$	-20	A
Maximum Power Dissipation	$P_D$	1.4	W
Operating Junction and Storage Temperature Range	$T_J, T_{STG}$	-55 To 150	$^\circ C$

### Thermal Characteristic

Thermal Resistance, Junction-to-Ambient <sup>(Note 2)</sup>	$R_{\theta JA}$	100	$^\circ C/W$
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• Static Electrical Characteristics @ T<sub>J</sub> = 25°C (unless otherwise stated)

Parameter	Symbol	Condition	Min	Typ	Max	Unit
<b>Off Characteristics</b>						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V I <sub>D</sub> =-250μA	-30	--	--	V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =-30V V <sub>GS</sub> =0V	--	--	-1	μA
Gate-Body Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±20V V <sub>DS</sub> =0V	--	--	±100	nA
<b>On Characteristics</b> (Note 3)						
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> I <sub>D</sub> =-250μA	-1.0	-1.6	-2.5	V
Drain-Source On-State Resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> =-10V I <sub>D</sub> =-2.6A	--	77	87	mΩ
		V <sub>GS</sub> =-4.5V I <sub>D</sub> =-2A	--	85	100	mΩ
<b>Dynamic Characteristics</b> (Note 4)						
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> =-15V V <sub>GS</sub> =0V F=1.0MHz	--	197	--	PF
Output Capacitance	C <sub>oss</sub>		--	42	--	PF
Reverse Transfer Capacitance	C <sub>rss</sub>		--	26	--	PF
<b>Switching Characteristics</b> (Note 4)						
Turn-on Delay Time	t <sub>d(on)</sub>	V <sub>DD</sub> =-15V I <sub>D</sub> =-2.6A V <sub>GS</sub> =-10V R <sub>G</sub> =3Ω,	--	7.5	--	nS
Turn-on Rise Time	t <sub>r</sub>		--	4.1	--	nS
Turn-Off Delay Time	t <sub>d(off)</sub>		--	11.8	--	nS
Turn-Off Fall Time	t <sub>f</sub>		--	3.8	--	nS
Total Gate Charge	Q <sub>g</sub>	V <sub>DS</sub> =-15V I <sub>D</sub> =-2.6A V <sub>GS</sub> =-10V	--	4.3	--	nC
Gate-Source Charge	Q <sub>gs</sub>		--	0.7	--	nC
Gate-Drain Charge	Q <sub>gd</sub>		--	1.1	--	nC
<b>Drain-Source Diode Characteristics</b>						
Diode Forward Voltage (Note 3)	V <sub>SD</sub>	V <sub>GS</sub> =0V I <sub>S</sub> =-2.6A	--	-0.84	-1.2	V
Diode Forward Current (Note 2)	I <sub>S</sub>		--	--	-2.6	A

Notes:

- ① Pulse width limited by maximum allowable junction temperature
- ② Pulse test ; Pulse width ≤ 300μs, duty cycle ≤ 2%.

• Typical Characteristics

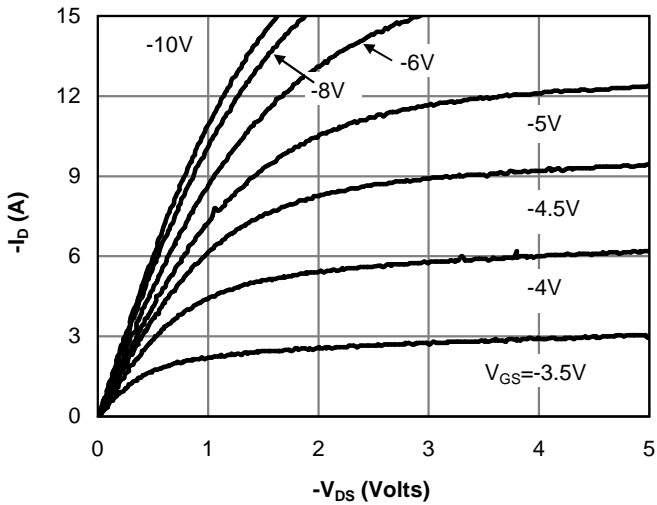


Fig 1: On-Region Characteristics (Note E)

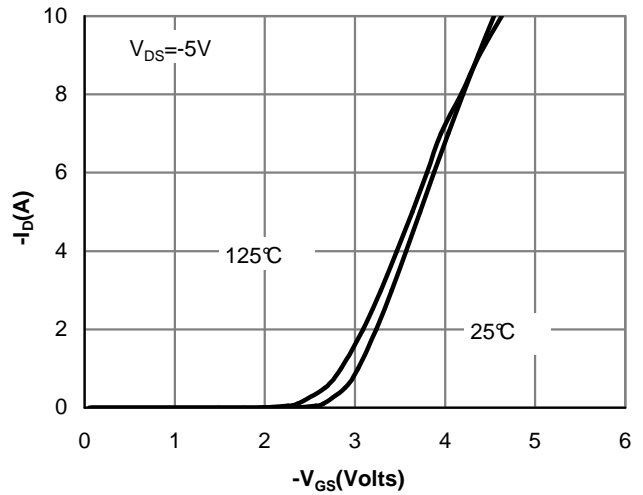


Figure 2: Transfer Characteristics (Note E)

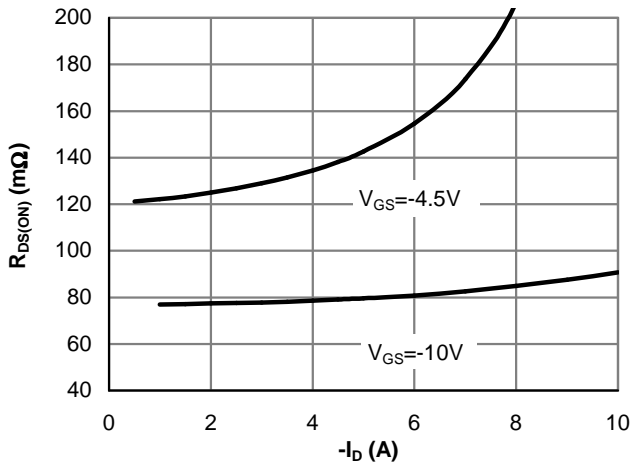


Figure 3: On-Resistance vs. Drain Current and Gate Voltage (Note E)

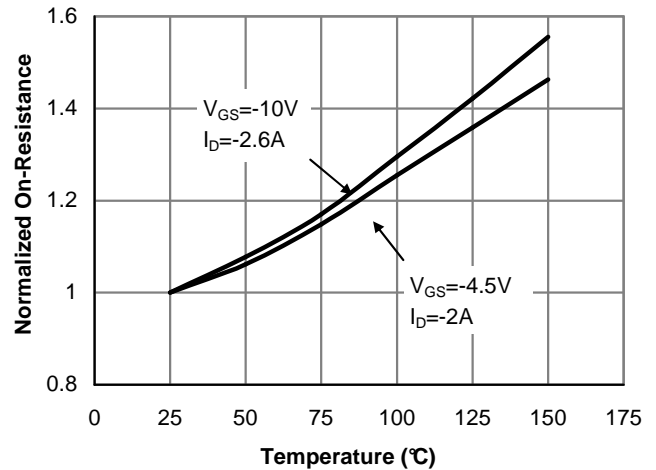


Figure 4: On-Resistance vs. Junction Temperature (Note E)

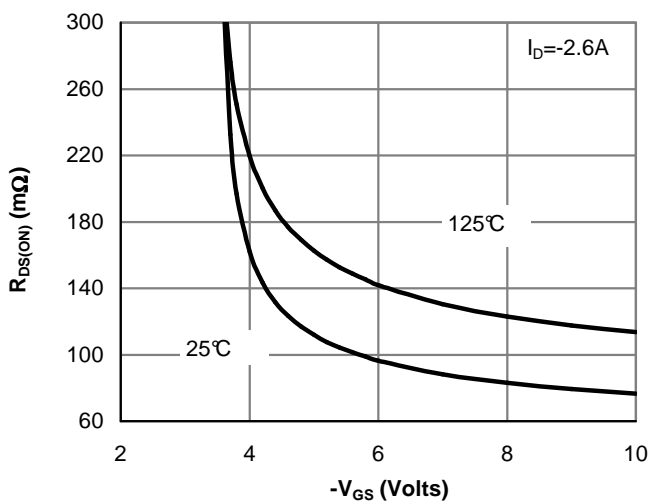


Figure 5: On-Resistance vs. Gate-Source Voltage (Note E)

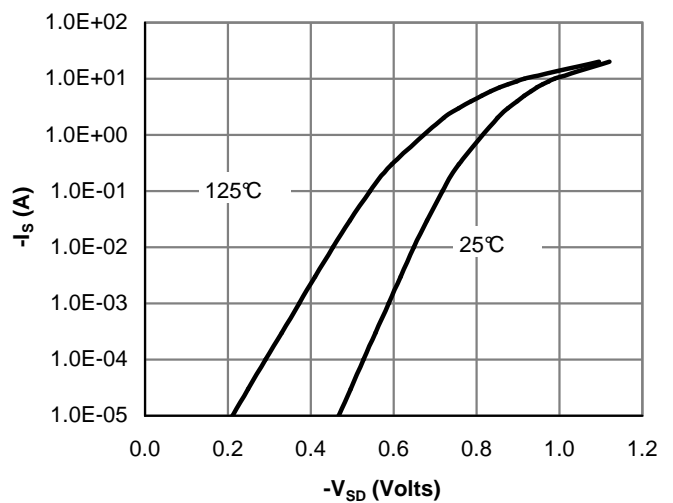


Figure 6: Body-Diode Characteristics (Note E)

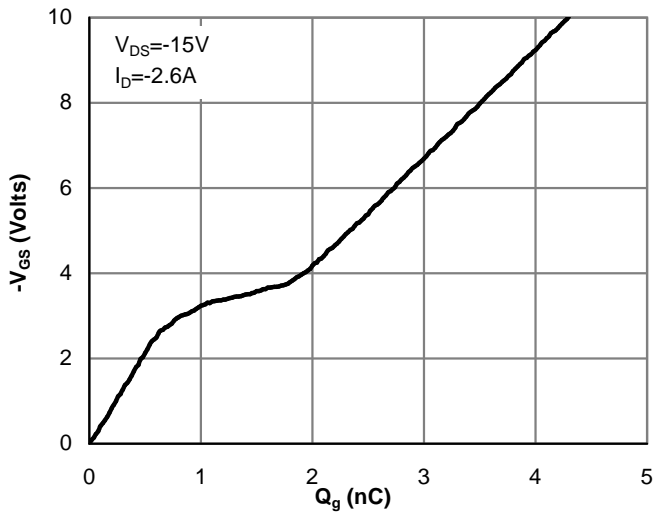


Figure 7: Gate-Charge Characteristics

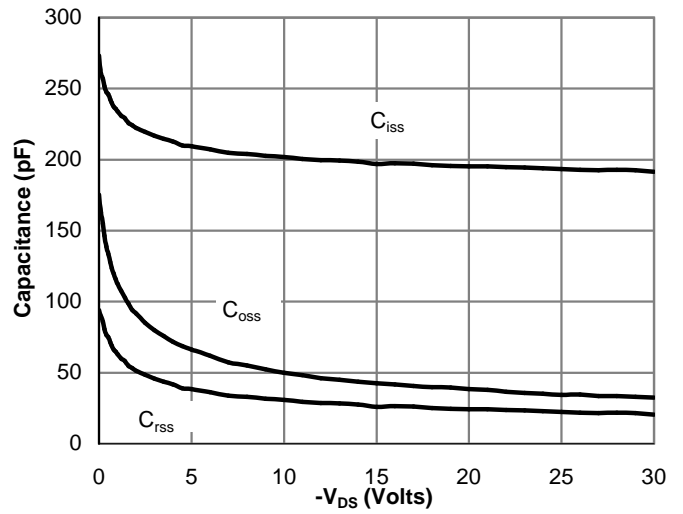


Figure 8: Capacitance Characteristics

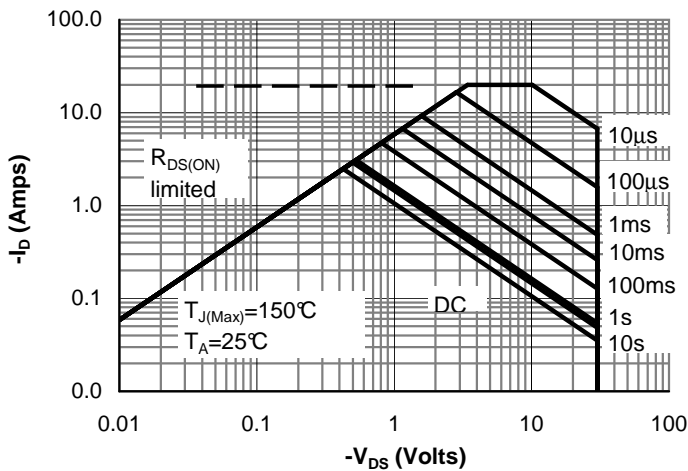


Figure 9: Maximum Forward Biased Safe Operating Area (Note F)

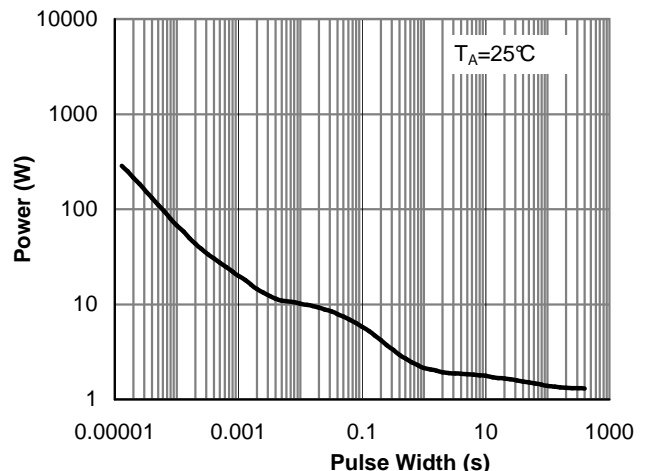


Figure 10: Single Pulse Power Rating Junction-to-Ambient (Note F)

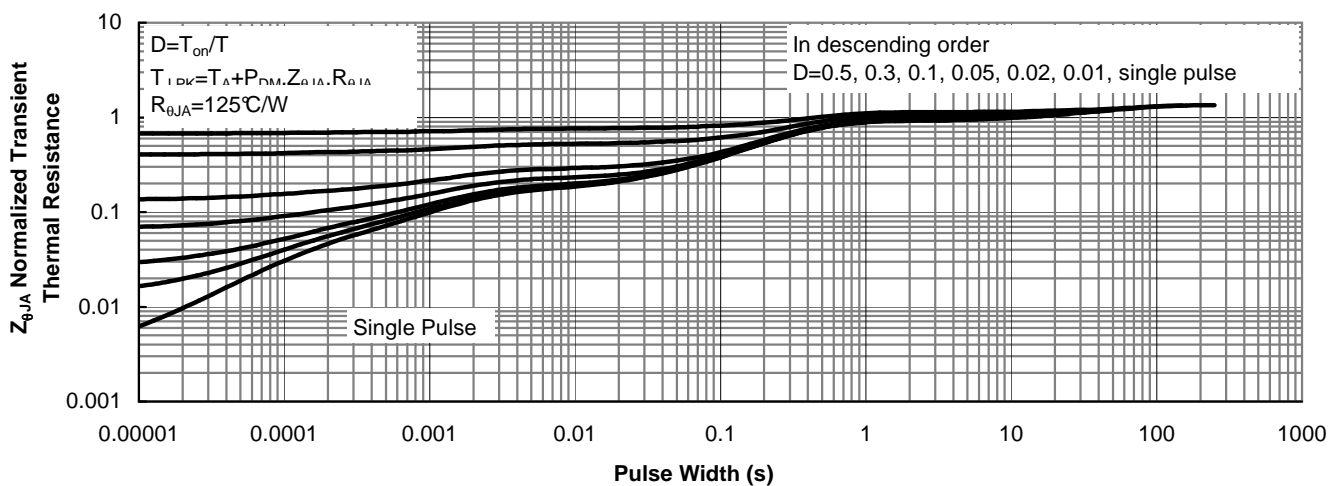
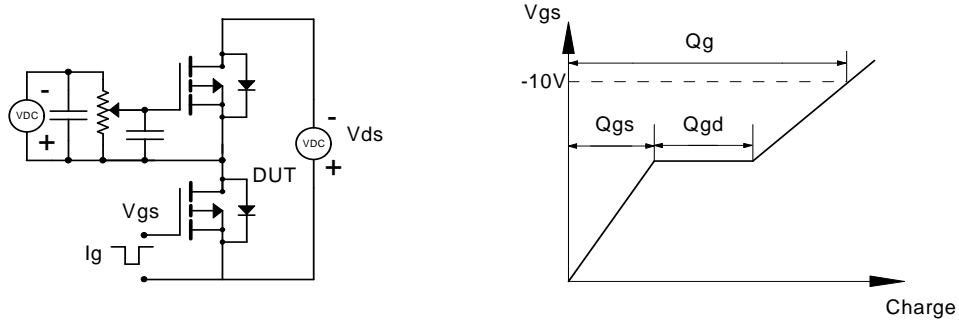


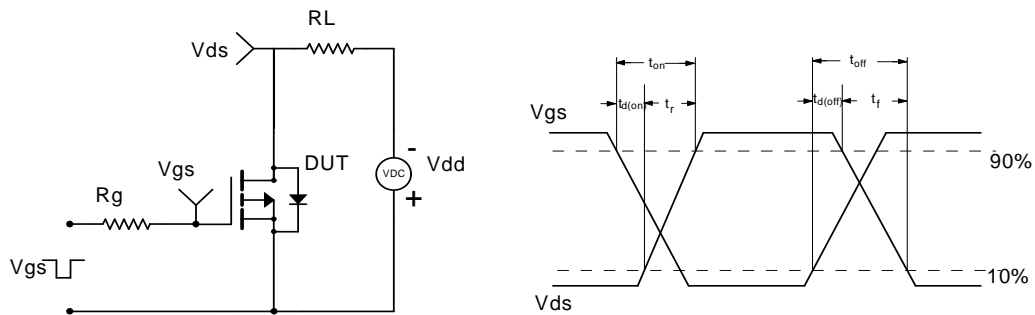
Figure 11: Normalized Maximum Transient Thermal Impedance (Note F)

• Test circuit

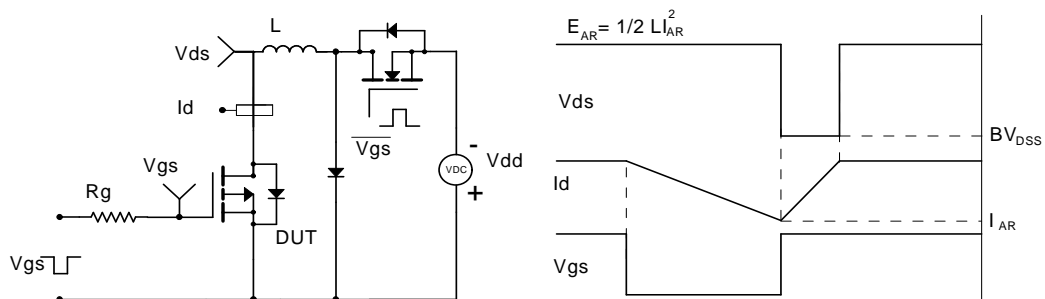
Gate Charge Test Circuit & Waveform



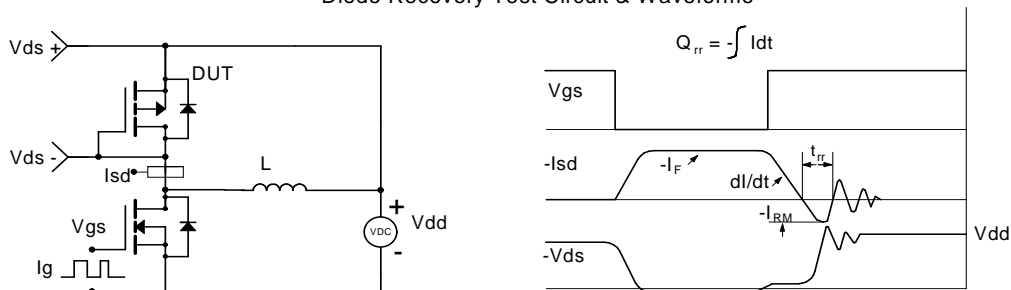
Resistive Switching Test Circuit & Waveforms



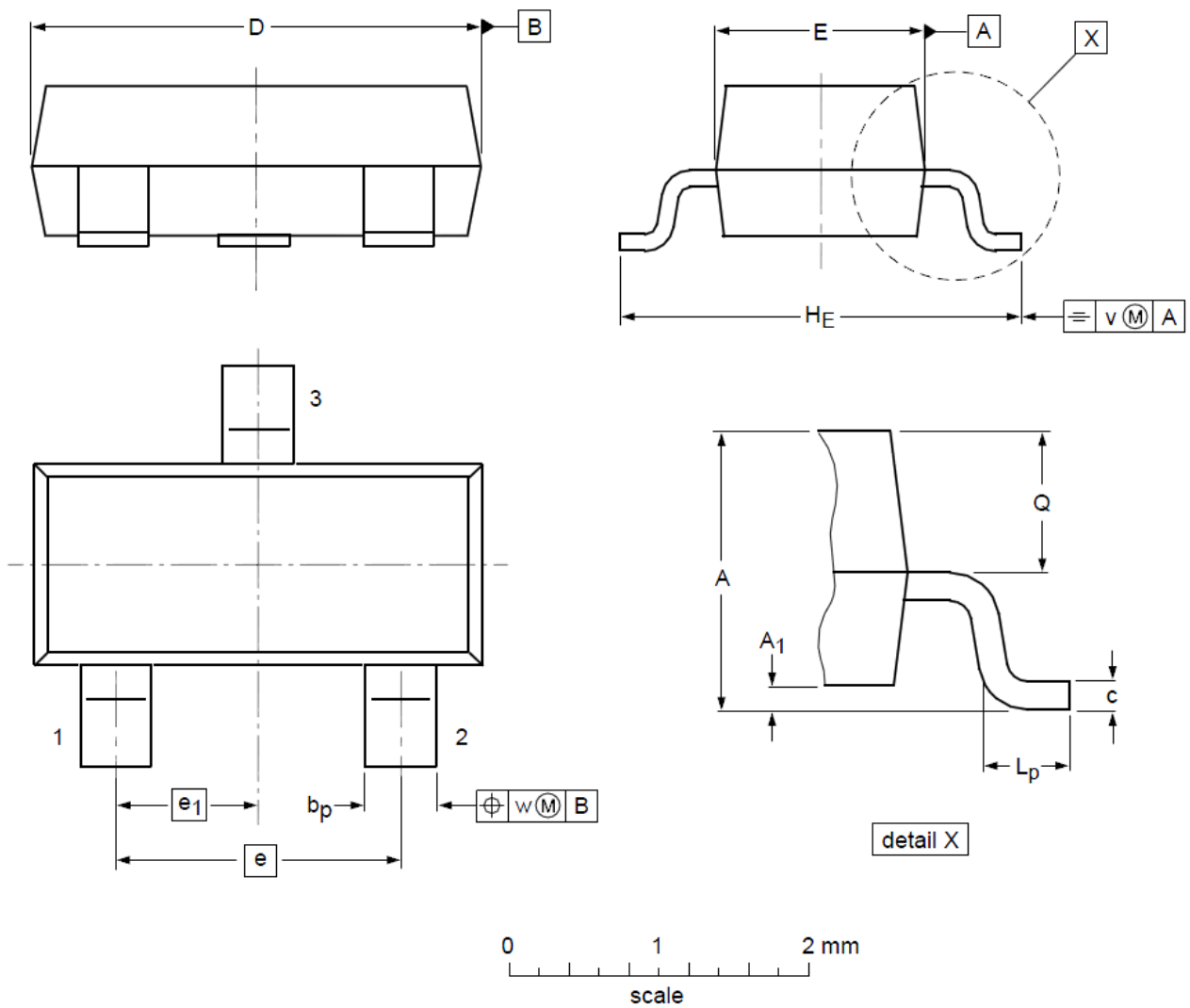
Unclamped Inductive Switching (UIS) Test Circuit & Waveforms



Diode Recovery Test Circuit & Waveforms



## SOT23-3L Package Outline Dimensions



### DIMENSIONS ( unit : mm )

Symbol	Min	Typ	Max	Symbol	Min	Typ	Max
A	1.00	1.17	1.30	A <sub>1</sub>	0.01	0.05	0.10
b <sub>p</sub>	0.35	0.39	0.50	c	0.10	0.20	0.26
D	2.70	2.90	3.10	E	1.30	1.58	1.70
e	--	1.90	--	e <sub>1</sub>	--	0.95	--
H <sub>E</sub>	2.50	2.78	3.00	L <sub>p</sub>	0.20	0.32	0.60
Q	0.23	0.27	0.33	v	--	0.20	--
w	--	0.20	--				