

• Product Summary

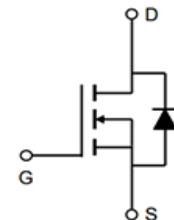
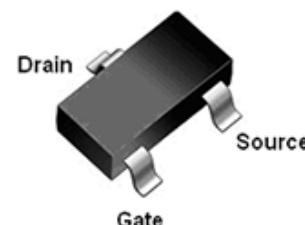
Part #	V _{DS}	R _{DS(on).typ} (@V _{GS} =10V)	R _{DS(on).typ} (@V _{GS} =4.5V)	I _D
BSS138	50V	0.88Ω	1.5Ω	0.22A

• Features

- High density cell design for extremely low R_{DS(on)}
- Rugged and Reliable

• Application

- Direct Logic-Level Interface: TTL/CMOS
- Drivers: Relays, Solenoids, Lamps, Hammers; Display,
- Memories, Transistors, etc.
- Battery Operated Systems
- Solid-State Relays


N-Channel MOSFET

SOT- 23

• Ordering Information:

Part NO.	BSS138
Marking	SS
Packing Information	REEL TAPE
Basic ordering unit (pcs)	3000

• Absolute Maximum Ratings (T_C=25°C)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V _{DS}	50	V
Gate-Source Voltage	V _{GS}	±20	V
Drain Current-Continuous	I _D	0.22	A
Drain Current-Pulsed (Note 1)	I _{DM}	0.88	A
Maximum Power Dissipation	P _D	0.35	W
Operating Junction and Storage Temperature Range	T _J , T _{STG}	-55 To 150	°C

• Thermal Characteristic

Thermal Resistance, Junction-to-Ambient (Note 2)	R _{θJA}	357	°C/W
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• Static Electrical Characteristics @ $T_J = 25^\circ\text{C}$ (unless otherwise stated)

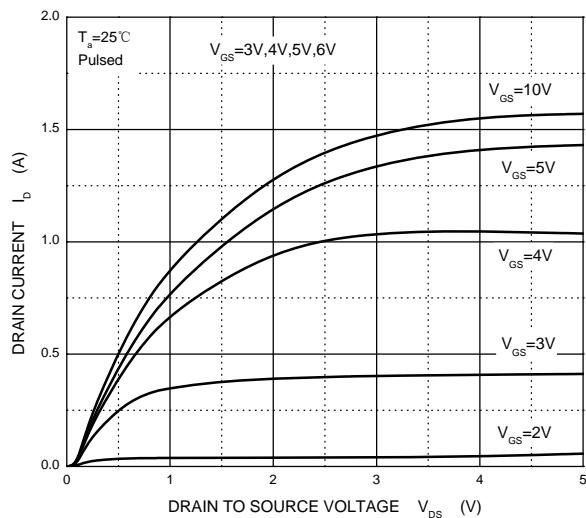
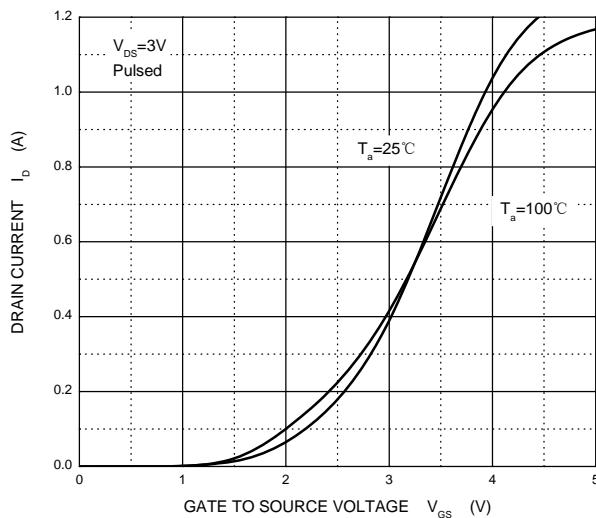
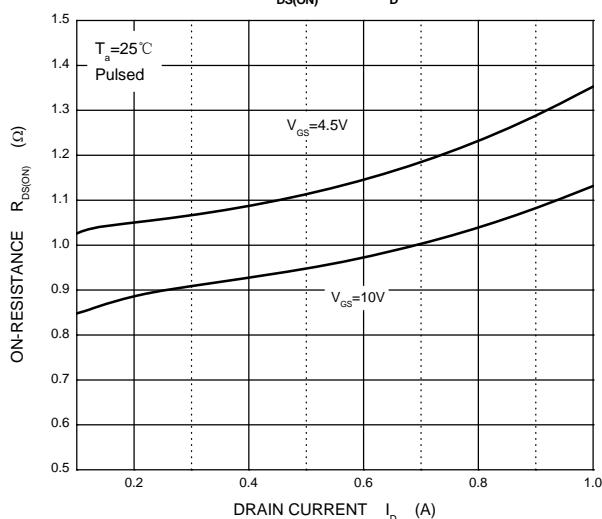
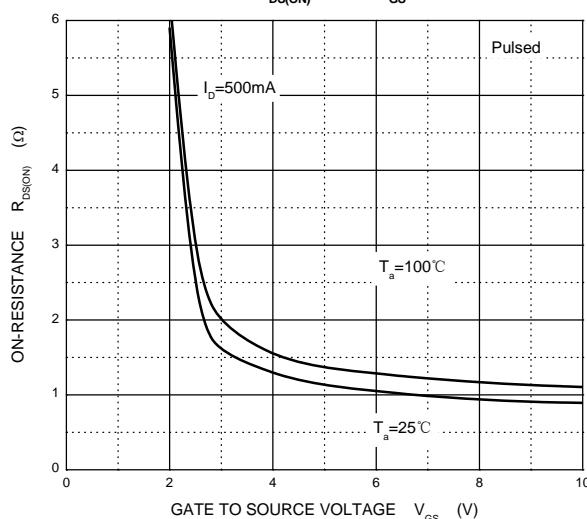
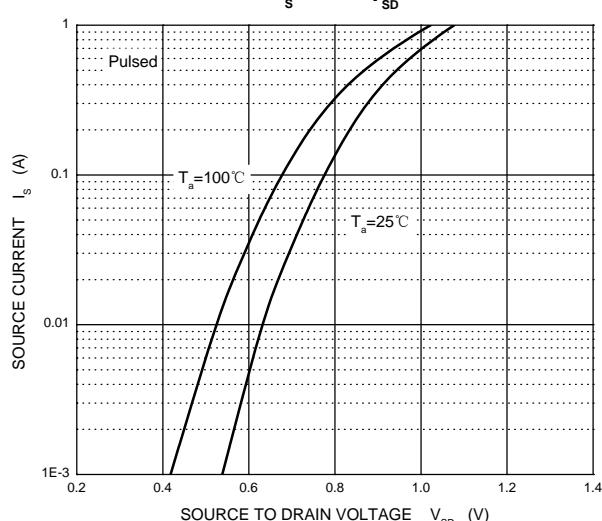
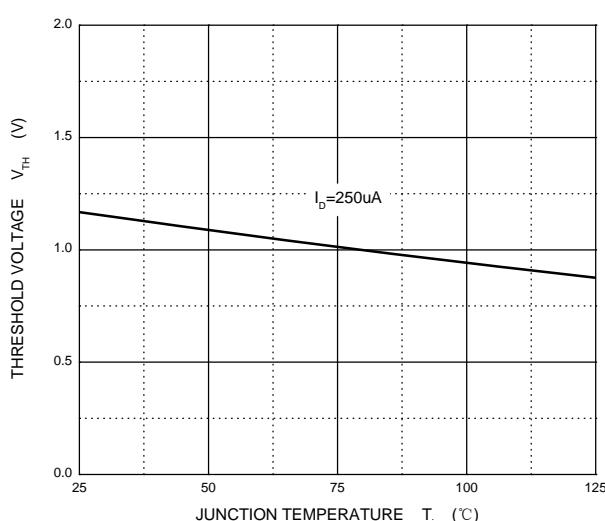
Parameter	Symbol	Condition	Min	Typ	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{\text{GS}}=0\text{V} I_{\text{D}}=250\mu\text{A}$	50	--	--	V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{\text{DS}}=50\text{V} V_{\text{GS}}=0\text{V}$	--	--	0.5	μA
Gate-Body Leakage Current	I_{GSS}	$V_{\text{GS}}=\pm 20\text{V} V_{\text{DS}}=0\text{V}$	--	--	± 100	nA
On Characteristics <small>(Note 3)</small>						
Gate Threshold Voltage	$V_{\text{GS(th)}}$	$V_{\text{DS}}=V_{\text{GS}} I_{\text{D}}=1\text{mA}$	0.8	--	1.5	V
Drain-Source On-State Resistance	$R_{\text{DS(ON)}}$	$V_{\text{GS}}=10\text{V} I_{\text{D}}=0.22\text{A}$	--	0.88	3.5	Ω
		$V_{\text{GS}}=4.5\text{V} I_{\text{D}}=0.22\text{A}$	--	1.5	6	Ω
Dynamic Characteristics <small>(Note 4)</small>						
Input Capacitance	C_{iss}	$V_{\text{DS}}=25\text{V} V_{\text{GS}}=0\text{V}$ $F=1.0\text{MHz}$	--	27	--	PF
Output Capacitance	C_{oss}		--	13	--	PF
Reverse Transfer Capacitance	C_{rss}		--	6	--	PF
Forward Transconductance	g_{fs}	$V_{\text{DS}}=10\text{V}, I_{\text{D}}=0.22\text{A}$	0.12	--	--	mS
Switching Characteristics <small>(Note 4)</small>						
Turn-on Delay Time	$t_{\text{d(on)}}$	$V_{\text{DD}}=30\text{V} I_{\text{D}}=0.29\text{A}$ $V_{\text{GS}}=5\text{V} R_{\text{G}}=6\Omega,$	--	--	5	nS
Turn-on Rise Time	t_r		--	--	18	nS
Turn-Off Delay Time	$t_{\text{d(off)}}$		--	--	36	nS
Turn-Off Fall Time	t_f		--	--	14	nS
Drain-Source Diode Characteristics						
Diode Forward Voltage <small>(Note 3)</small>	V_{SD}	$V_{\text{GS}}=0\text{V} I_{\text{S}}=0.44\text{A}$	--	--	1.4	V

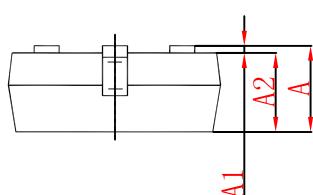
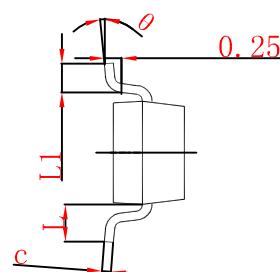
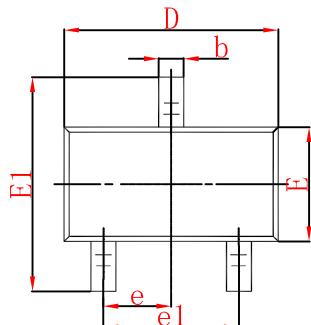
Notes:

① Pulse width limited by maximum allowable junction temperature

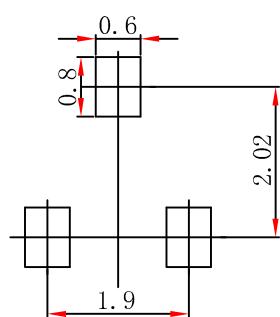
② Pulse test ; Pulse width $\leq 300\mu\text{s}$, duty cycle $\leq 2\%$.

• Typical Characteristics

Output Characteristics

Transfer Characteristics

 $R_{DS(ON)}$ — I_D

 $R_{DS(ON)}$ — V_{GS}

 I_S — V_{SD}

Threshold Voltage


SOT-23 Package Outline Dimensions


Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	0.900	1.150	0.035	0.045
A1	0.000	0.100	0.000	0.004
A2	0.900	1.050	0.035	0.041
b	0.300	0.500	0.012	0.020
c	0.080	0.150	0.003	0.006
D	2.800	3.000	0.110	0.118
E	1.200	1.400	0.047	0.055
E1	2.250	2.550	0.089	0.100
e	0.950 TYP		0.037 TYP	
e1	1.800	2.000	0.071	0.079
L	0.550 REF		0.022 REF	
L1	0.300	0.500	0.012	0.020
θ	0°	8°	0°	8°


Note:

1. Controlling dimension: in millimeters.
2. General tolerance: $\pm 0.05\text{mm}$.
3. The pad layout is for reference purposes only.