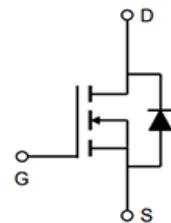


**• Product Summary**

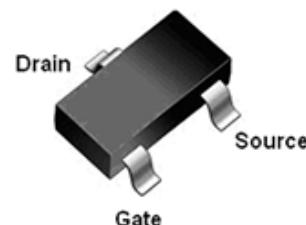
Part #	V <sub>DS</sub>	R <sub>DS(on).typ</sub> (@V <sub>GS</sub> =10V)	R <sub>DS(on).typ</sub> (@V <sub>GS</sub> =4.5V)	I <sub>D</sub>
EFM6005	60V	38mΩ	50mΩ	5A


**• Features**

- Low R<sub>DS(on)</sub> @ V<sub>GS</sub>=10V
- 4.5V Logic Level Control
- N Channel SOT23 Package
- Pb-Free, RoHS Compliant

**N-Channel MOSFET**
**• Application**

- Load Switch
- Battery switch
- DC/DC Converter


**• Ordering Information:**

Part NO.	EFM6005
Marking	****
Packing Information	REEL TAPE
Basic ordering unit (pcs)	3000

**HF**
**• Absolute Maximum Ratings (T<sub>C</sub>=25°C)**

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V <sub>DS</sub>	60	V
Gate-Source Voltage	V <sub>GS</sub>	±20	V
Drain Current-Continuous	I <sub>D</sub>	5	A
Drain Current-Pulsed (Note 1)	I <sub>DM</sub>	16	A
Maximum Power Dissipation	P <sub>D</sub>	1.7	W
Operating Junction and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-55 To 150	°C

**• Thermal Characteristic**

Thermal Resistance, Junction-to-Ambient (Note 2)	R <sub>θJA</sub>	74	°C/W
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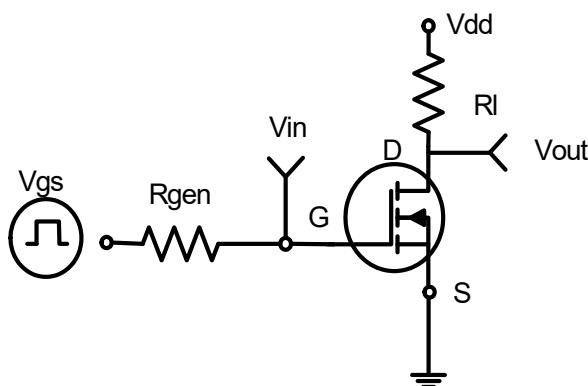
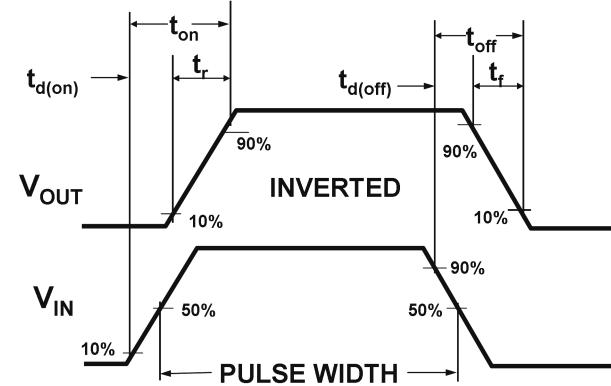
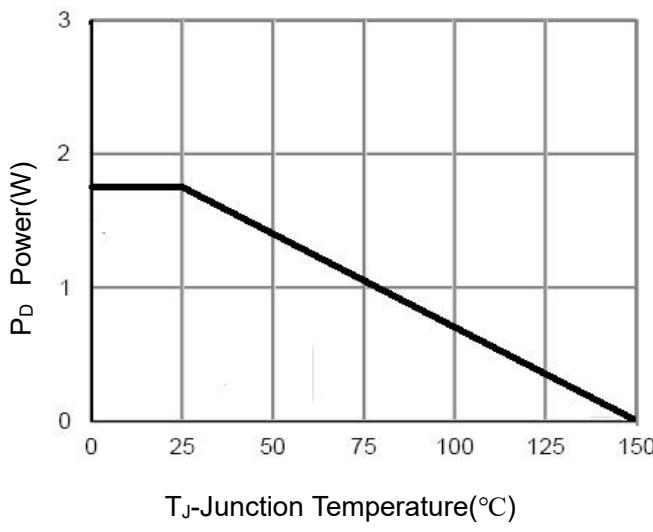
**• Static Electrical Characteristics @  $T_J = 25^\circ C$  (unless otherwise stated)**

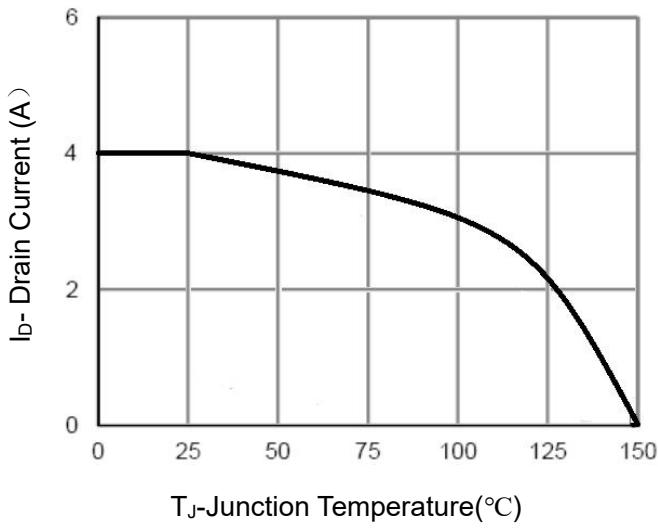
Parameter	Symbol	Condition	Min	Typ	Max	Unit
<b>Off Characteristics</b>						
Drain-Source Breakdown Voltage	$V_{DSS}$	$V_{GS}=0V I_D=250\mu A$	60	--	--	V
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS}=60V V_{GS}=0V$	--	--	1	$\mu A$
Gate-Body Leakage Current	$I_{GSS}$	$V_{GS}=\pm 20V V_{DS}=0V$	--	--	$\pm 100$	nA
<b>On Characteristics</b> <small>(Note 3)</small>						
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS} I_D=250\mu A$	1.2	1.8	2.5	V
Drain-Source On-State Resistance	$R_{DS(ON)}$	$V_{GS}=10V I_D=5A$	--	38	48	$m\Omega$
		$V_{GS}=4.5V I_D=4A$	--	50	65	$m\Omega$
<b>Dynamic Characteristics</b> <small>(Note 4)</small>						
Input Capacitance	$C_{iss}$	$V_{DS}=30V V_{GS}=0V$ $F=1.0MHz$	--	553	--	PF
Output Capacitance	$C_{oss}$		--	43	--	PF
Reverse Transfer Capacitance	$C_{rss}$		--	37	--	PF
<b>Switching Characteristics</b> <small>(Note 4)</small>						
Turn-on Delay Time	$t_{d(on)}$	$V_{DD}=30V I_D=4A$ $V_{GS}=10V R_G=1\Omega$	--	6	--	nS
Turn-on Rise Time	$t_r$		--	11	--	nS
Turn-Off Delay Time	$t_{d(off)}$		--	18	--	nS
Turn-Off Fall Time	$t_f$		--	10	--	nS
Total Gate Charge	$Q_g$	$V_{DS}=30V I_D=4A$ $V_{GS}=10V$	--	11	--	nC
Gate-Source Charge	$Q_{gs}$		--	1.7	--	nC
Gate-Drain Charge	$Q_{gd}$		--	2.6	--	nC
<b>Drain-Source Diode Characteristics</b>						
Diode Forward Voltage <small>(Note 3)</small>	$V_{SD}$	$V_{GS}=0V I_S=4A$	--	--	1.2	V
Diode Forward Current <small>(Note 2)</small>	$I_S$		--	--	4	A

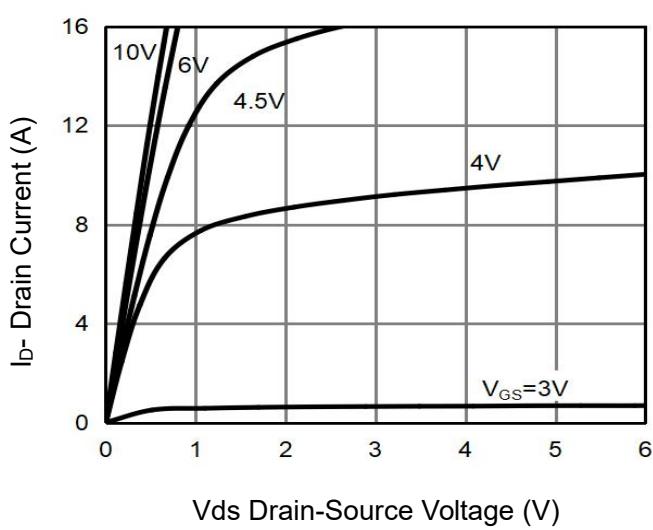
**Notes:**

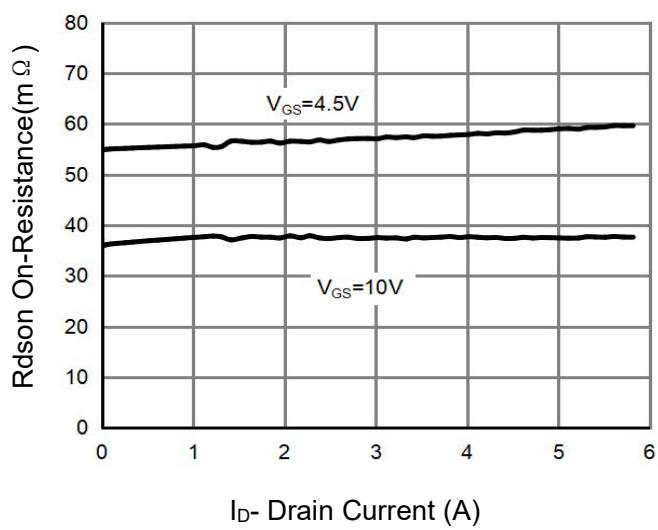
1. Repetitive Rating: Pulse width limited by maximum junction temperature.
2. The value of  $R \theta JA$  is measured with the device mounted on 1in 2 FR-4 board with 2oz. Copper, in a still air environment with  $T_A=25^\circ C$ . The value in any given application depends on the user's specific board design.
3. Pulse Test: Pulse Width  $\leq 300\mu s$ , Duty Cycle  $\leq 2\%$ .
4. Guaranteed by design, not subject to production
5. EAS condition :  $T_J=25^\circ C, V_{DD}=30V, V_G=10V, L=0.5mH, R_g=25\Omega$

## • Typical Characteristics

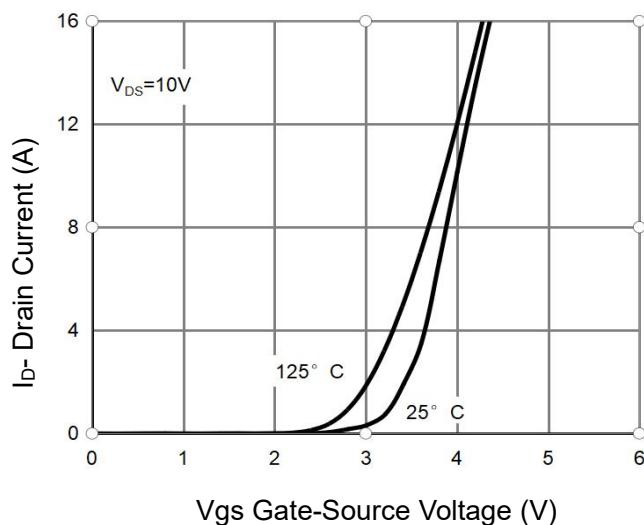
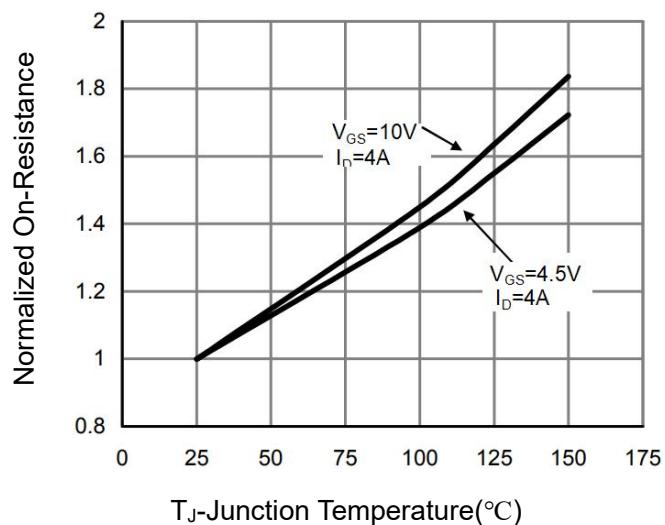
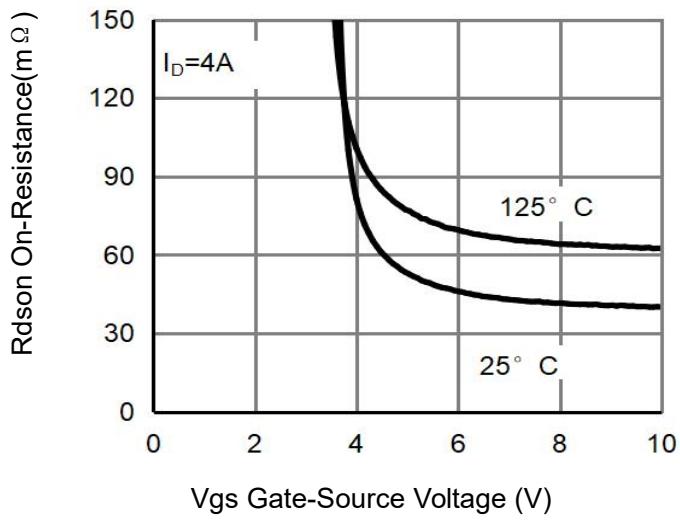
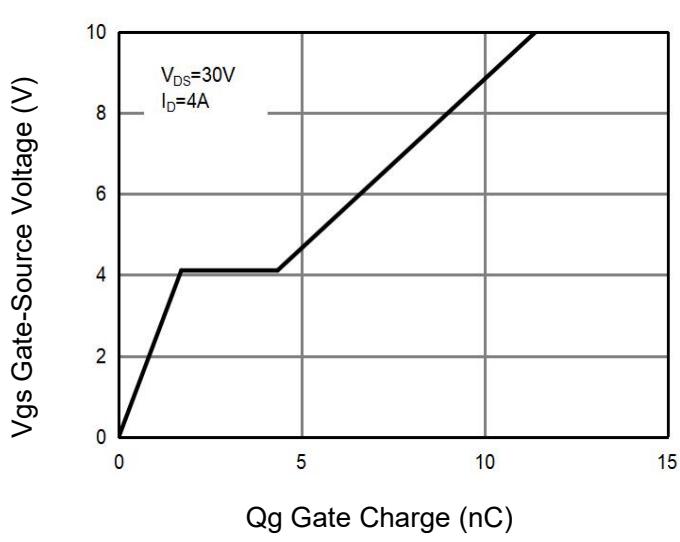
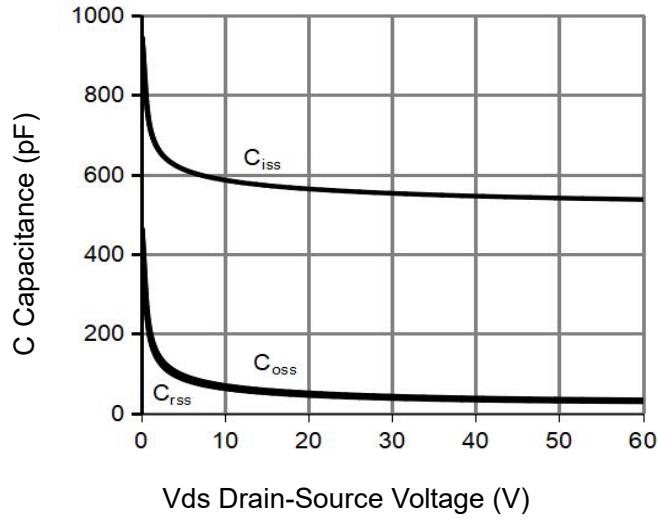
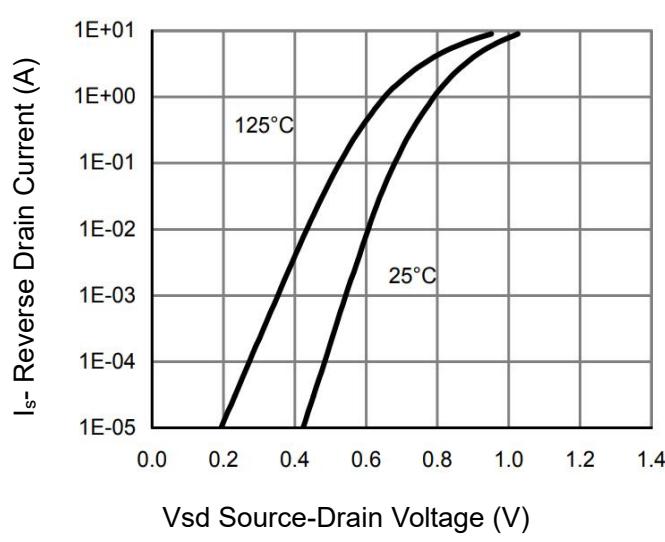

**Figure 1:Switching Test Circuit**

**Figure 2:Switching Waveforms**

 T<sub>j</sub>-Junction Temperature(°C)

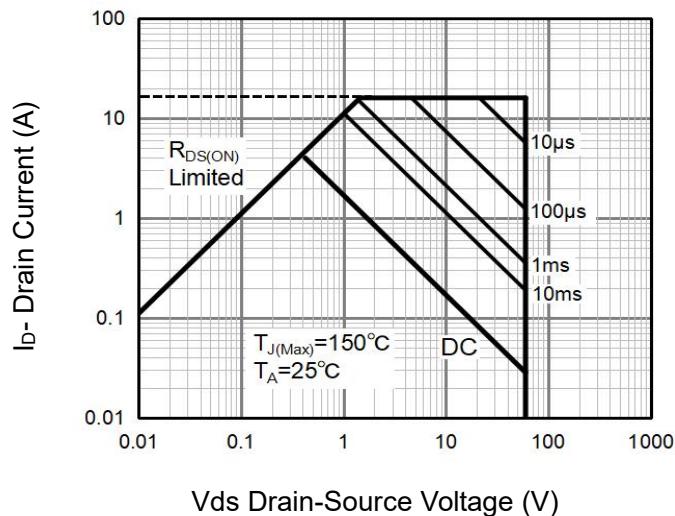
**Figure 3 Power Dissipation**

 T<sub>j</sub>-Junction Temperature(°C)

**Figure 4 Drain Current**

 V<sub>ds</sub> Drain-Source Voltage (V)

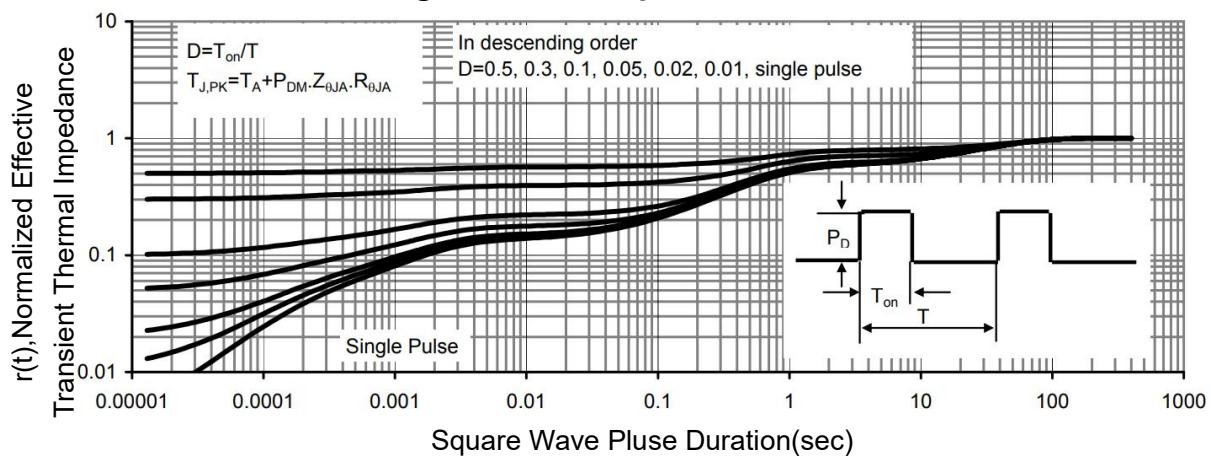
**Figure 5 Output Characteristics**

 I<sub>D</sub>- Drain Current (A)

**Figure 6 Drain-Source On-Resistance**

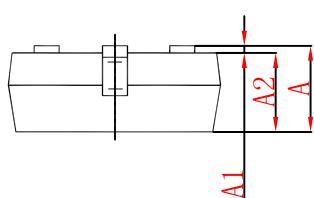
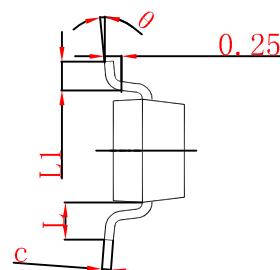
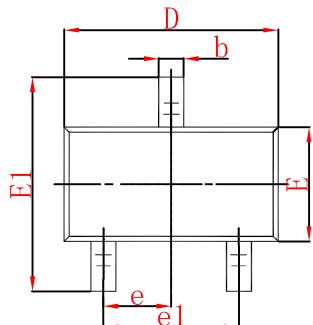

**Figure 7 Transfer Characteristics**

**Figure 8 Drain-Source On-Resistance**

**Figure 9 Rdson vs Vgs**

**Figure 11 Gate Charge**

**Figure 12 Source- Drain Diode Forward**



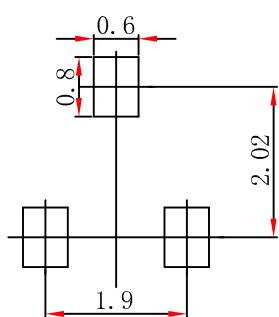
**Figure 13 Safe Operation Area**



**Figure 14 Normalized Maximum Transient Thermal Impedance**

**SOT-23 Package Outline Dimensions**


Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	0.900	1.150	0.035	0.045
A1	0.000	0.100	0.000	0.004
A2	0.900	1.050	0.035	0.041
b	0.300	0.500	0.012	0.020
c	0.080	0.150	0.003	0.006
D	2.800	3.000	0.110	0.118
E	1.200	1.400	0.047	0.055
E1	2.250	2.550	0.089	0.100
e	0.950 TYP		0.037 TYP	
e1	1.800	2.000	0.071	0.079
L	0.550 REF		0.022 REF	
L1	0.300	0.500	0.012	0.020
θ	0°	8°	0°	8°


**Note:**

1. Controlling dimension: in millimeters.
2. General tolerance:  $\pm 0.05\text{mm}$ .
3. The pad layout is for reference purposes only.