

• Product Summary

Part #	V _{DS}	R _{DS(on).typ} (@V _{GS} =10V)	R _{DS(on).typ} (@V _{GS} =4.5V)	I _D
EFM2309A	-60V	140mΩ	190mΩ	-2A

• Features

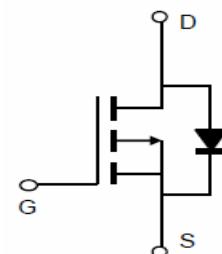
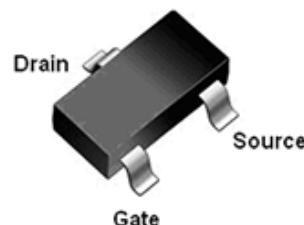
- Low R_{DS(on)} @ V_{GS}=-10V
- -5V Logic Level Control
- P Channel SOT23 Package
- Pb-Free, RoHS Compliant

• Application

- High-side Load Switch
- Switching Circuits
- High Speed line Driver

• Ordering Information:

Part NO.	EFM2309A
Marking	*****
Packing Information	REEL TAPE
Basic ordering unit (pcs)	3000


P-Channel MOSFET

HF
• Absolute Maximum Ratings (T_C=25°C)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V _{DS}	-60	V
Gate-Source Voltage	V _{GS}	±20	V
Drain Current-Continuous	I _D	-2	A
Drain Current-Pulsed (Note 1)	I _{DM}	-8	A
Maximum Power Dissipation	P _D	1	W
Operating Junction and Storage Temperature Range	T _J , T _{STG}	-55 To 150	°C

• Thermal Characteristic

Thermal Resistance, Junction-to-Ambient (Note 2)	R _{θJA}	125	°C/W
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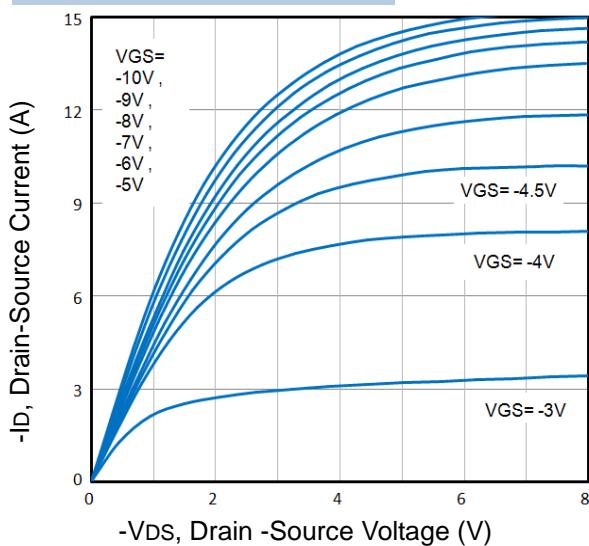
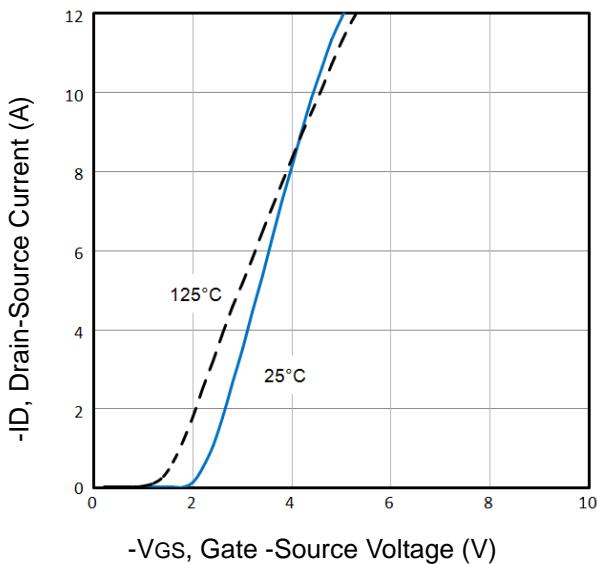
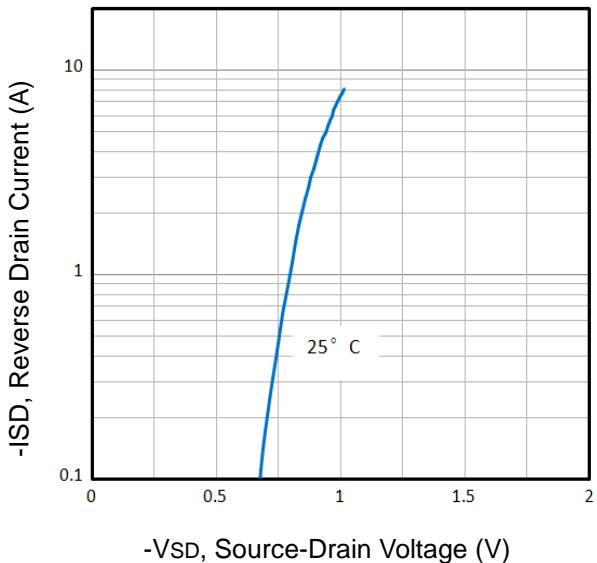
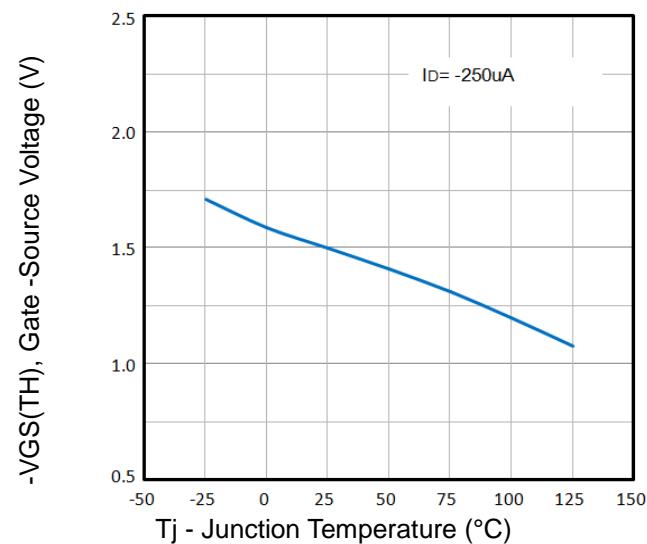
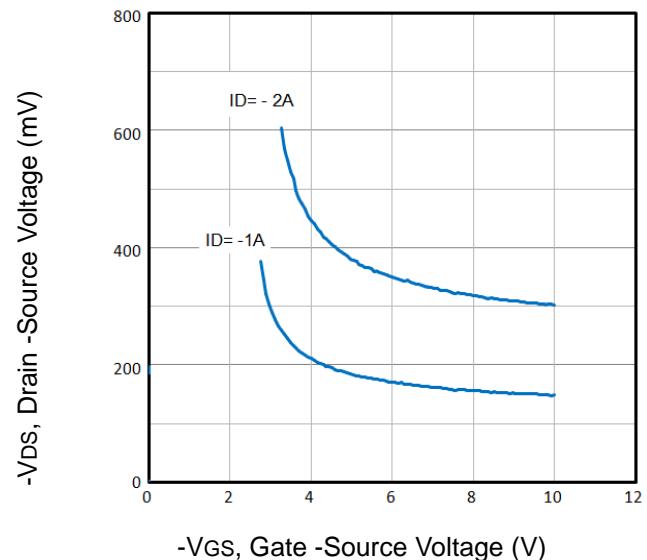
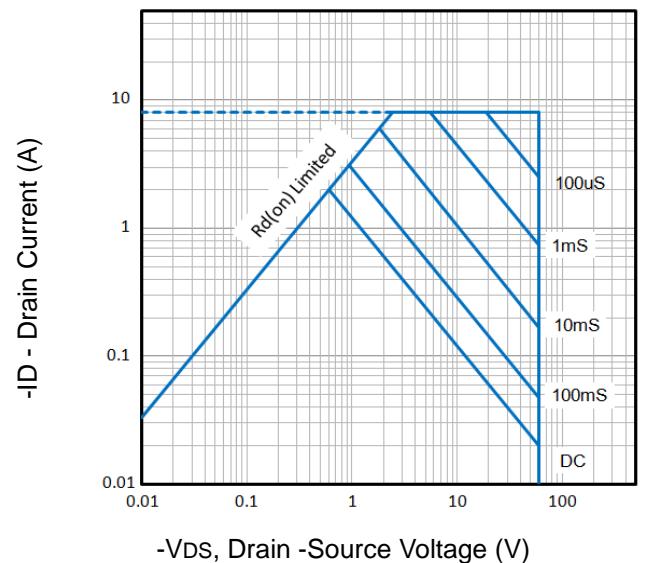
• Static Electrical Characteristics @ $T_J = 25^\circ\text{C}$ (unless otherwise stated)

Parameter	Symbol	Condition	Min	Typ	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{\text{GS}}=0\text{V} I_{\text{D}}=250\mu\text{A}$	-60	--	--	V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{\text{DS}}=-60\text{V} V_{\text{GS}}=0\text{V}$	--	--	-1	μA
Gate-Body Leakage Current	I_{GSS}	$V_{\text{GS}}=\pm 20\text{V} V_{\text{DS}}=0\text{V}$	--	--	± 100	nA
On Characteristics <small>(Note 3)</small>						
Gate Threshold Voltage	$V_{\text{GS(th)}}$	$V_{\text{DS}}=V_{\text{GS}} I_{\text{D}}=250\mu\text{A}$	-1.4	-2.0	-2.6	V
Drain-Source On-State Resistance	$R_{\text{DS(ON)}}$	$V_{\text{GS}}=-10\text{V} I_{\text{D}}=-2\text{A}$	--	140	190	$\text{m}\Omega$
		$V_{\text{GS}}=-4.5\text{V} I_{\text{D}}=-1.5\text{A}$	--	190	240	$\text{m}\Omega$
Dynamic Characteristics <small>(Note 4)</small>						
Input Capacitance	C_{iss}	$V_{\text{DS}}=-30\text{V} V_{\text{GS}}=0\text{V}$ $F=1.0\text{MHz}$	--	310	--	PF
Output Capacitance	C_{oss}		--	22	--	PF
Reverse Transfer Capacitance	C_{rss}		--	15	--	PF
Switching Characteristics <small>(Note 4)</small>						
Turn-on Delay Time	$t_{\text{d(on)}}$	$V_{\text{DD}}=-30\text{V} I_{\text{D}}=-2\text{A}$ $V_{\text{GS}}=-10\text{V} R_{\text{G}}=3.3\Omega$	--	41	--	nS
Turn-on Rise Time	t_r		--	22	--	nS
Turn-Off Delay Time	$t_{\text{d(off)}}$		--	25	--	nS
Turn-Off Fall Time	t_f		--	32	--	nS
Total Gate Charge	Q_g	$V_{\text{DS}}=-30\text{V} I_{\text{D}}=-2\text{A}$ $V_{\text{GS}}=-10\text{V}$	--	5.4	--	nC
Gate-Source Charge	Q_{gs}		--	1.1	--	nC
Gate-Drain Charge	Q_{gd}		--	1.6	--	nC
Drain-Source Diode Characteristics						
Diode Forward Voltage <small>(Note 3)</small>	V_{SD}	$V_{\text{GS}}=0\text{V} I_{\text{S}}=-2\text{A}$	--	-0.84	-1.2	V
Diode Forward Current <small>(Note 2)</small>	I_{S}		--	--	-1.4	A

Notes:

① Pulse width limited by maximum allowable junction temperature

② Pulse test ; Pulse width $\leq 300\mu\text{s}$, duty cycle $\leq 2\%$.

• Typical Characteristics

Fig1. Typical Output Characteristics

Fig3. Typical Transfer Characteristics

Fig5. Typical Source-Drain Diode Forward Voltage

Fig2. Normalized Threshold Voltage Vs. Temperature

Fig4. Drain -Source Voltage vs Gate -Source Voltage

Fig6. Maximum Safe Operating Area

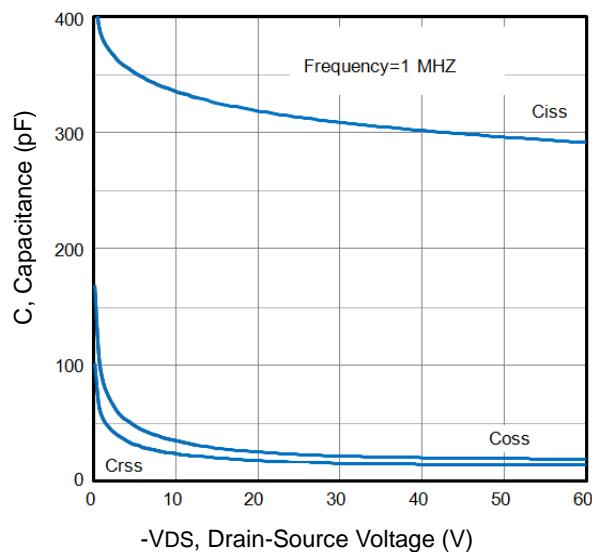


Fig7. Typical Capacitance Vs. Drain-Source Voltage

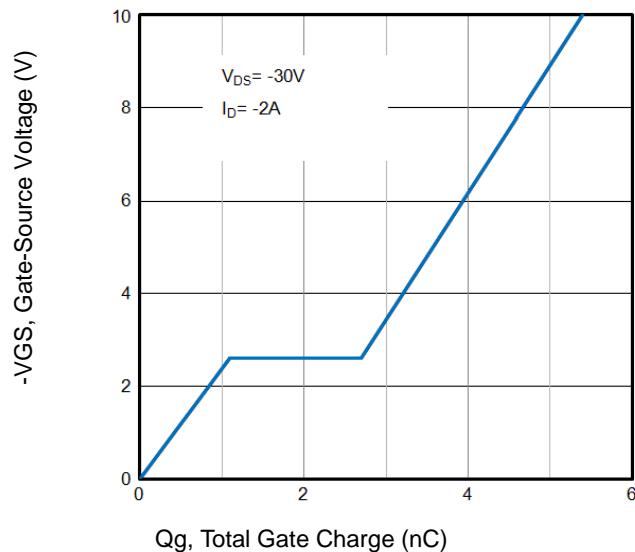


Fig8. Typical Gate Charge Vs. Gate-Source Voltage

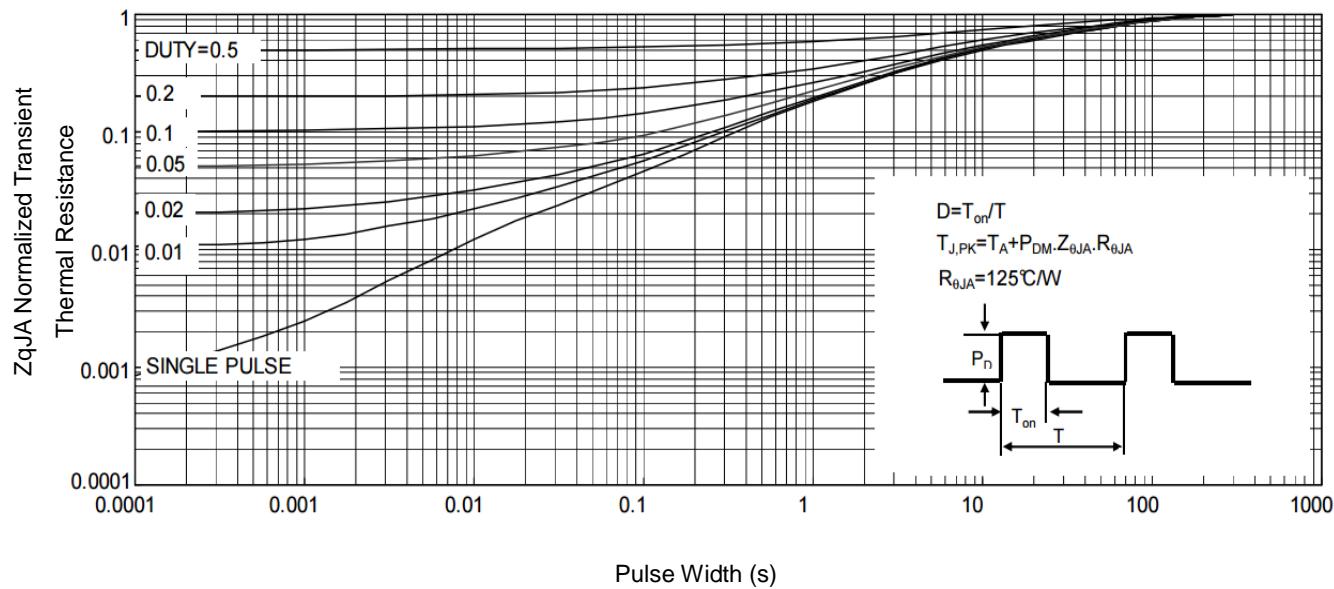


Fig9. Normalized Maximum Transient Thermal Impedance

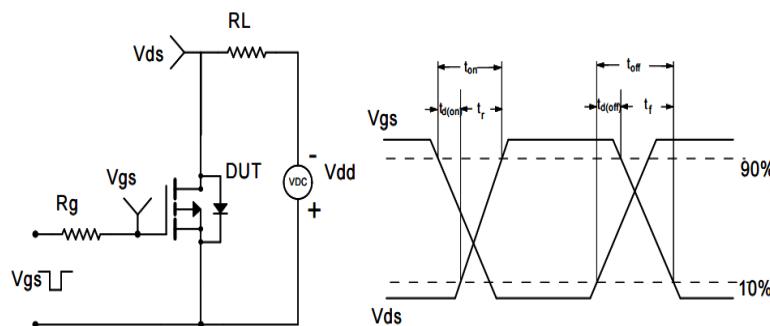
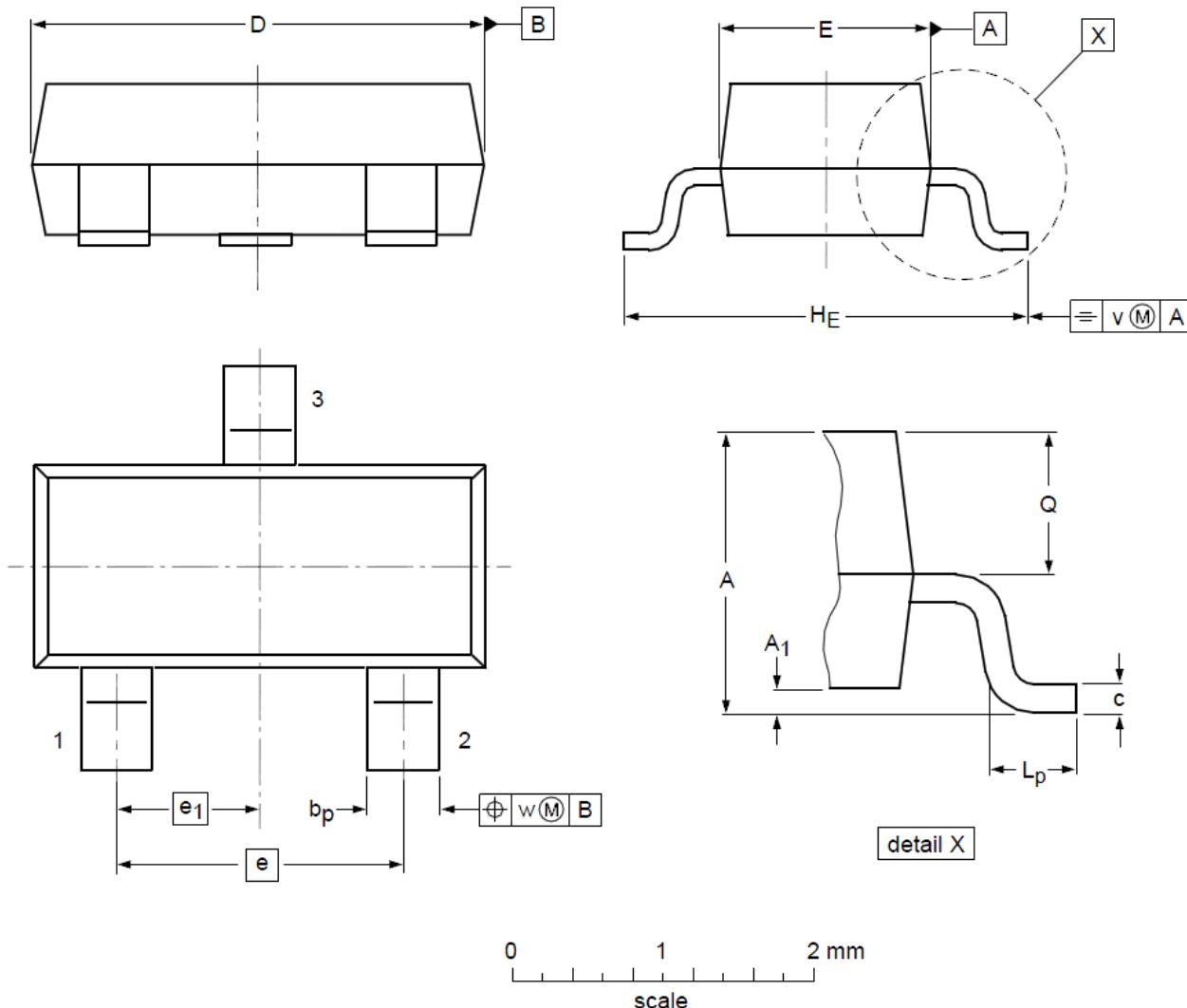


Fig10. Switching Time Test Circuit and waveforms

SOT23-3L Package Outline Dimensions

DIMENSIONS (unit : mm)

Symbol	Min	Typ	Max	Symbol	Min	Typ	Max
A	1.00	1.17	1.30	A₁	0.01	0.05	0.10
b_p	0.35	0.39	0.50	c	0.10	0.20	0.26
D	2.70	2.90	3.10	E	1.30	1.58	1.70
e	--	1.90	--	e₁	--	0.95	--
H_E	2.50	2.78	3.00	L_p	0.20	0.32	0.60
Q	0.23	0.27	0.33	v	--	0.20	--
w	--	0.20	--	M	--	--	--